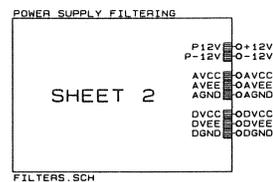
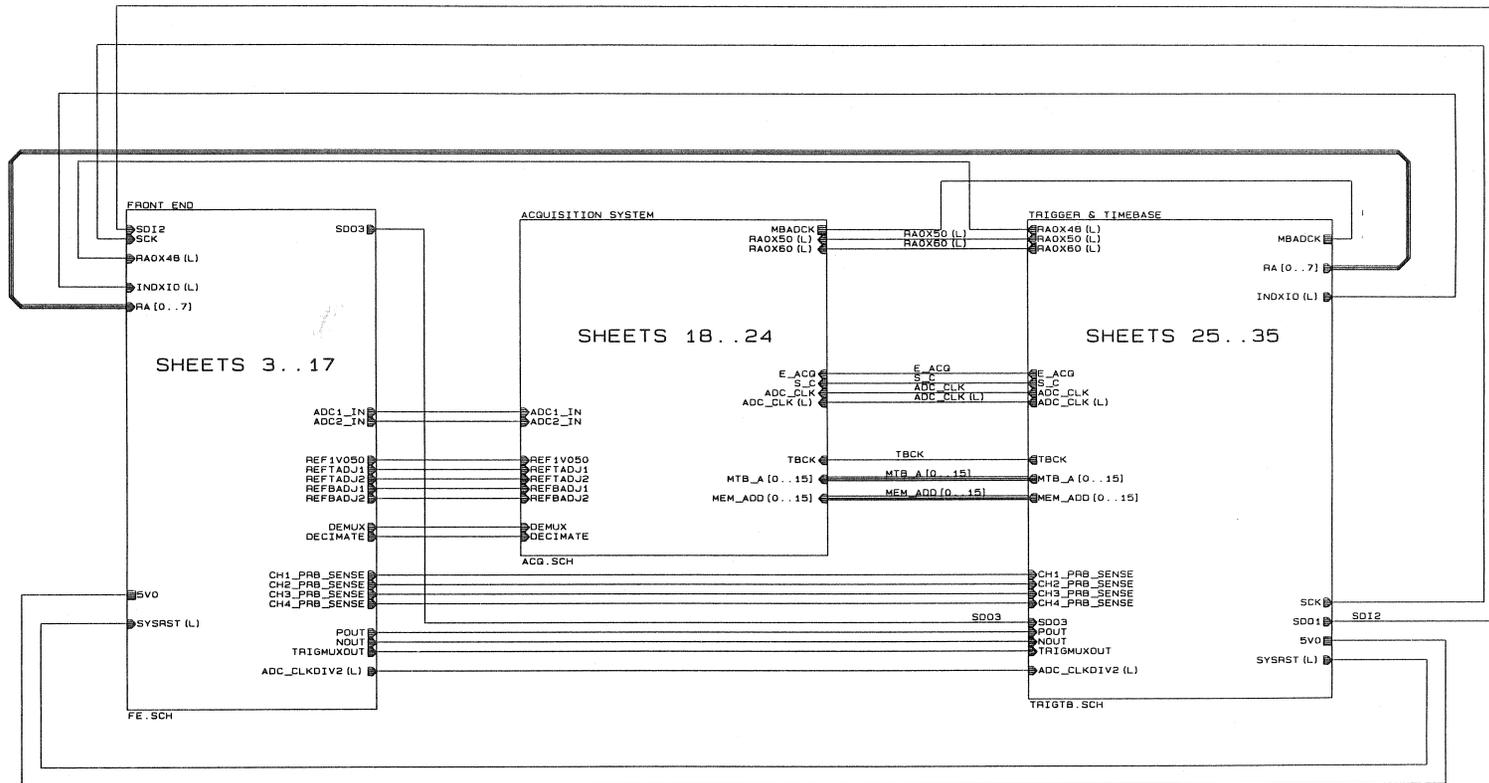


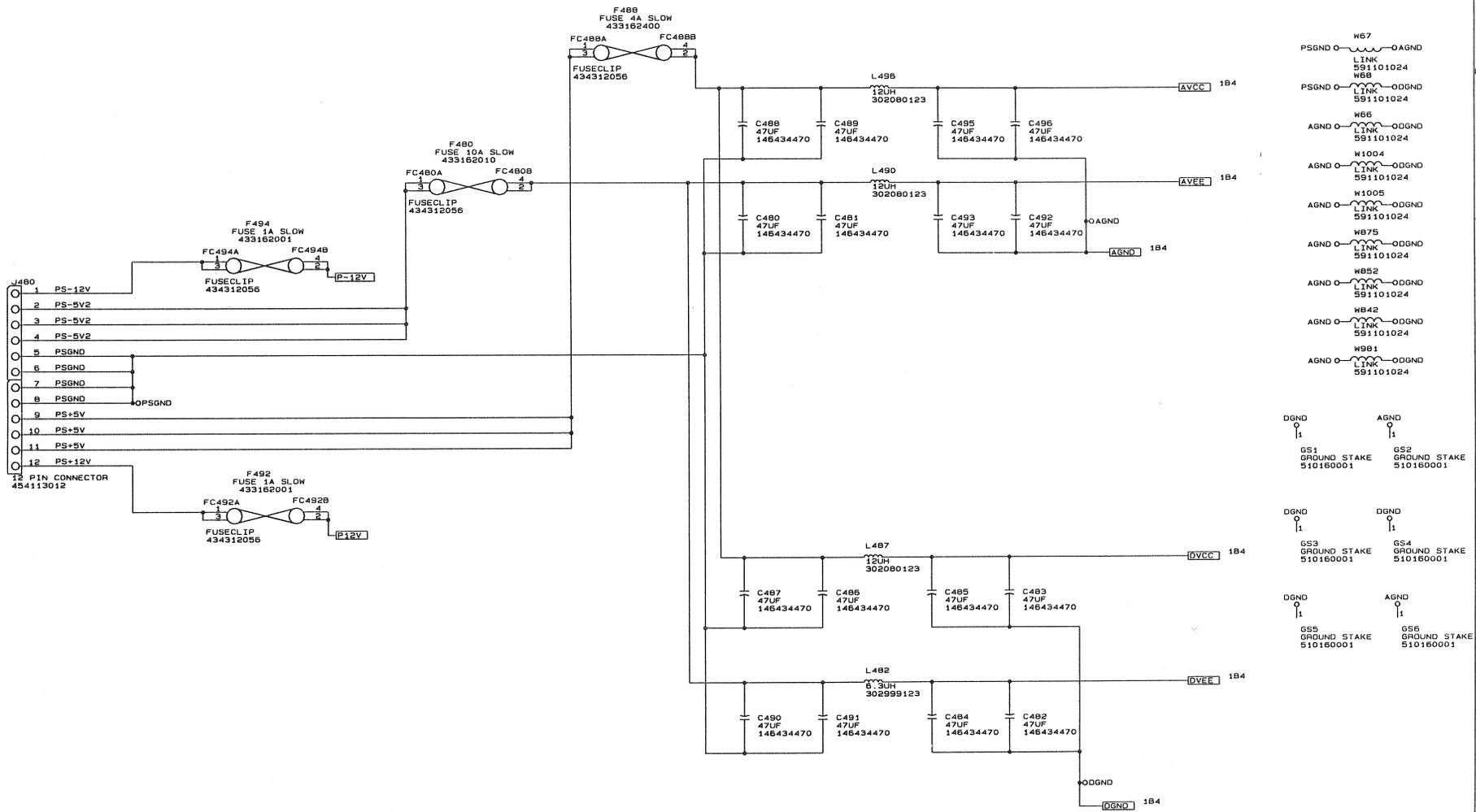
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LS140 SCHEMATIC



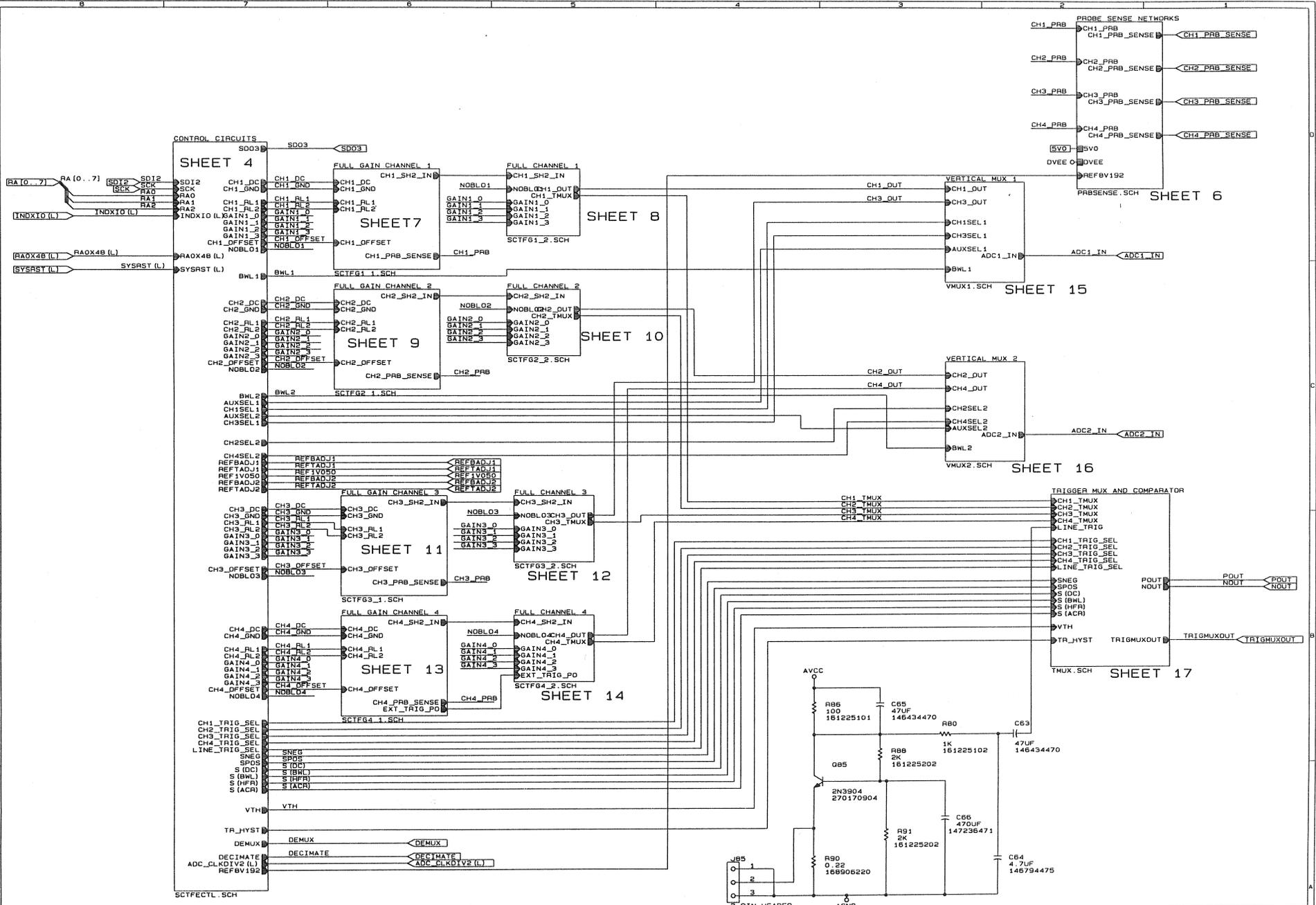
ECO: 724-F-1/1118
MCN: 724-F-1/002

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Title	
LS140 ACQUISITION BOARD 724-F-1	
Size	Document Number
C	MAIN.SCH
Date:	July 19, 1994 Sheet 1 of 35
REV	B



ECD: 724-F-1/1106

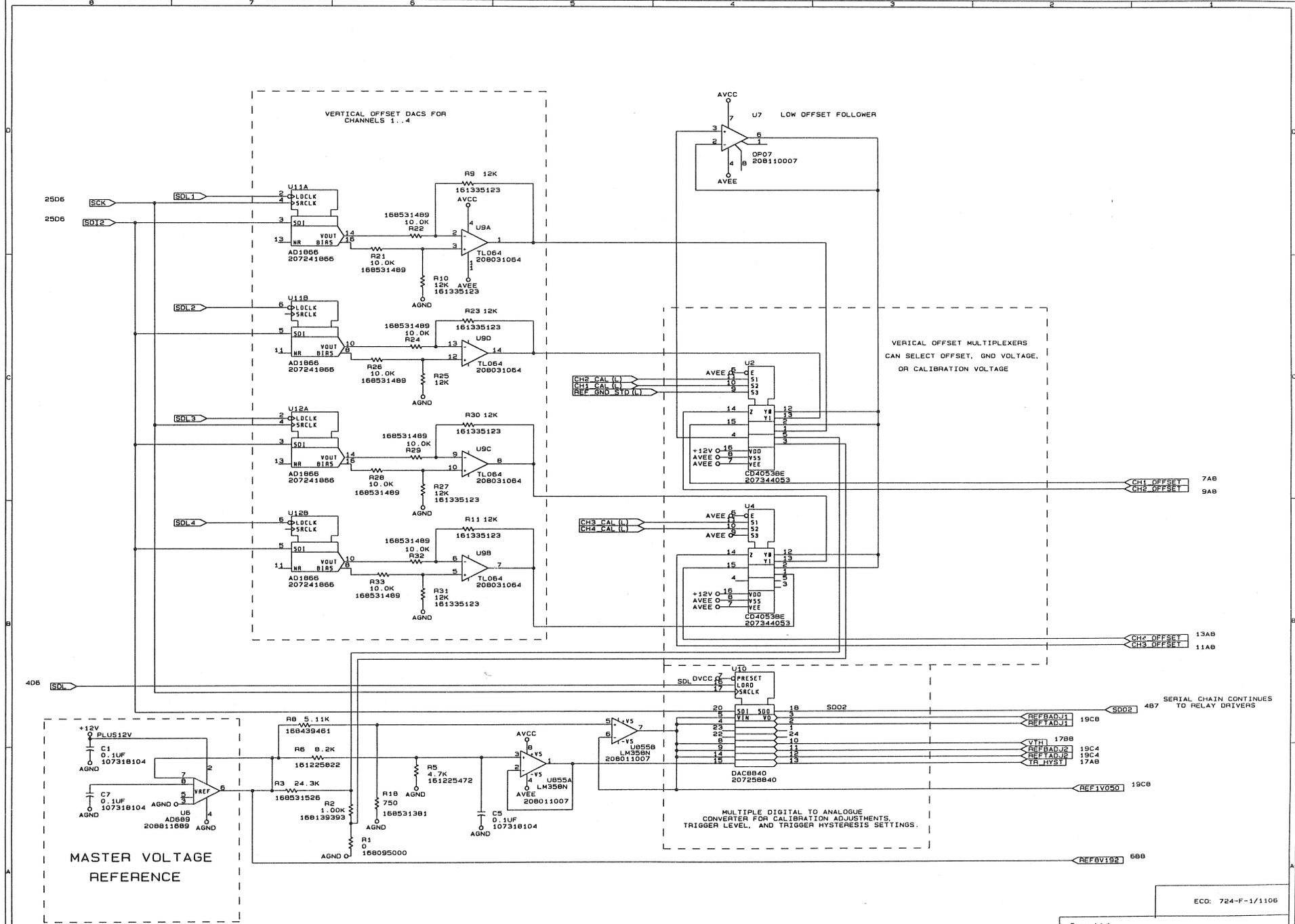
LeCroy Ltd.		REV
Notice: This design is the sole property of LeCroy Corp. and its subsidiaries. No use, reproduction or disclosure permitted without approval of LeCroy Corporation.		
Title		B
POWER SUPPLY FILTERING		
Size	Document Number	
C	FILTERS-SCH	
Date:	September 6, 1993	Sheet 2 of 32



LINE TRIGGER SENSING

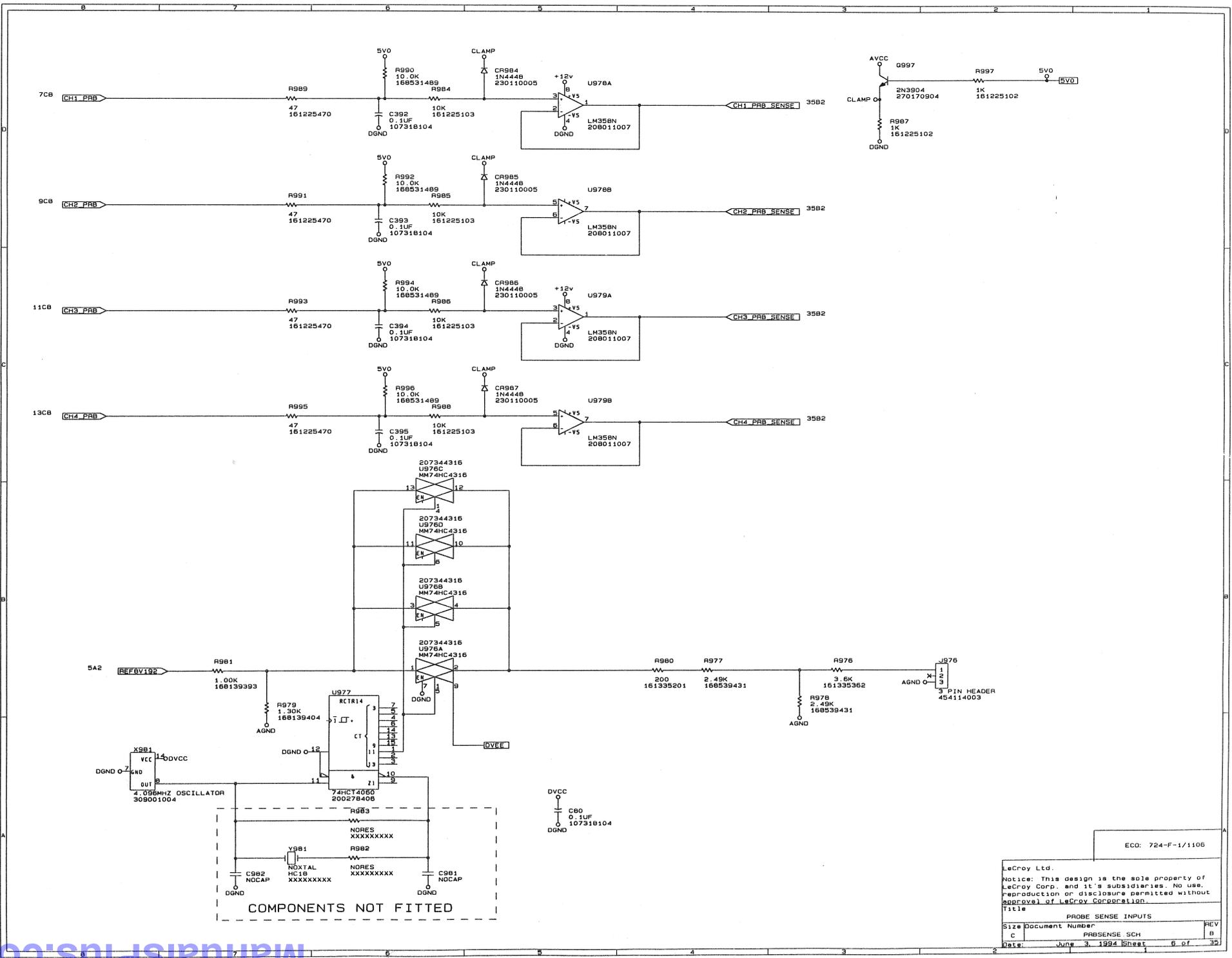
ECO: 724-F-1/1106

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 Size Document Number
 C FE SCH
 Date: September 8, 1993 Sheet 3 of 35



ECO: 724-F-1/1106

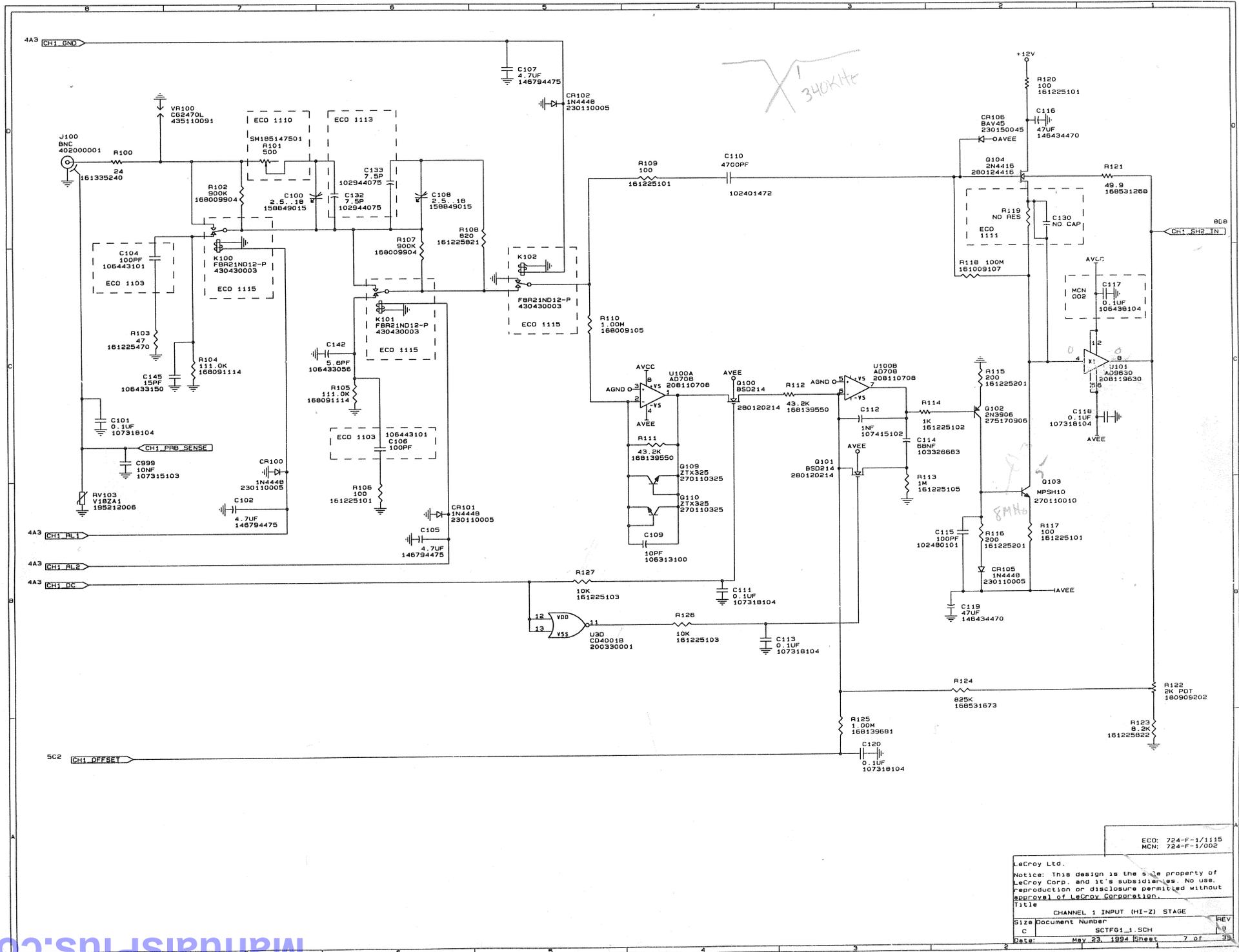
LeCroy Ltd.
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 Size Document Number: DACREF.SCH
 Date: September 8, 1993 Sheet: 35



ECO: 724-F-1/1106

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 Title

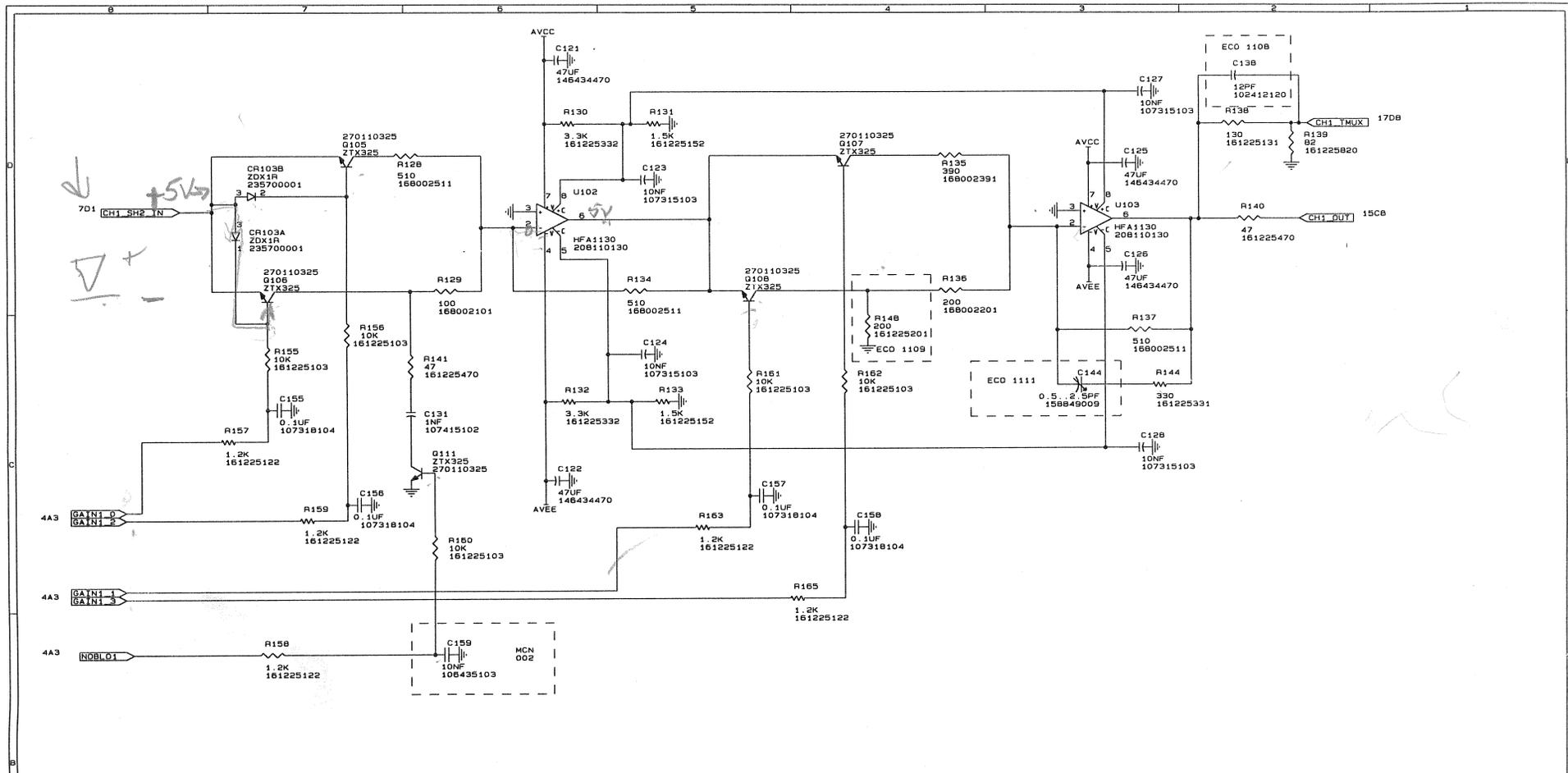
PROBE SENSE INPUTS		REV
Size	Document Number	B
C	PRBSNSE SCH	
Date	June 3, 1994	6 of 32



ECO: 724-F-1/1115
MCN: 724-F-1/002

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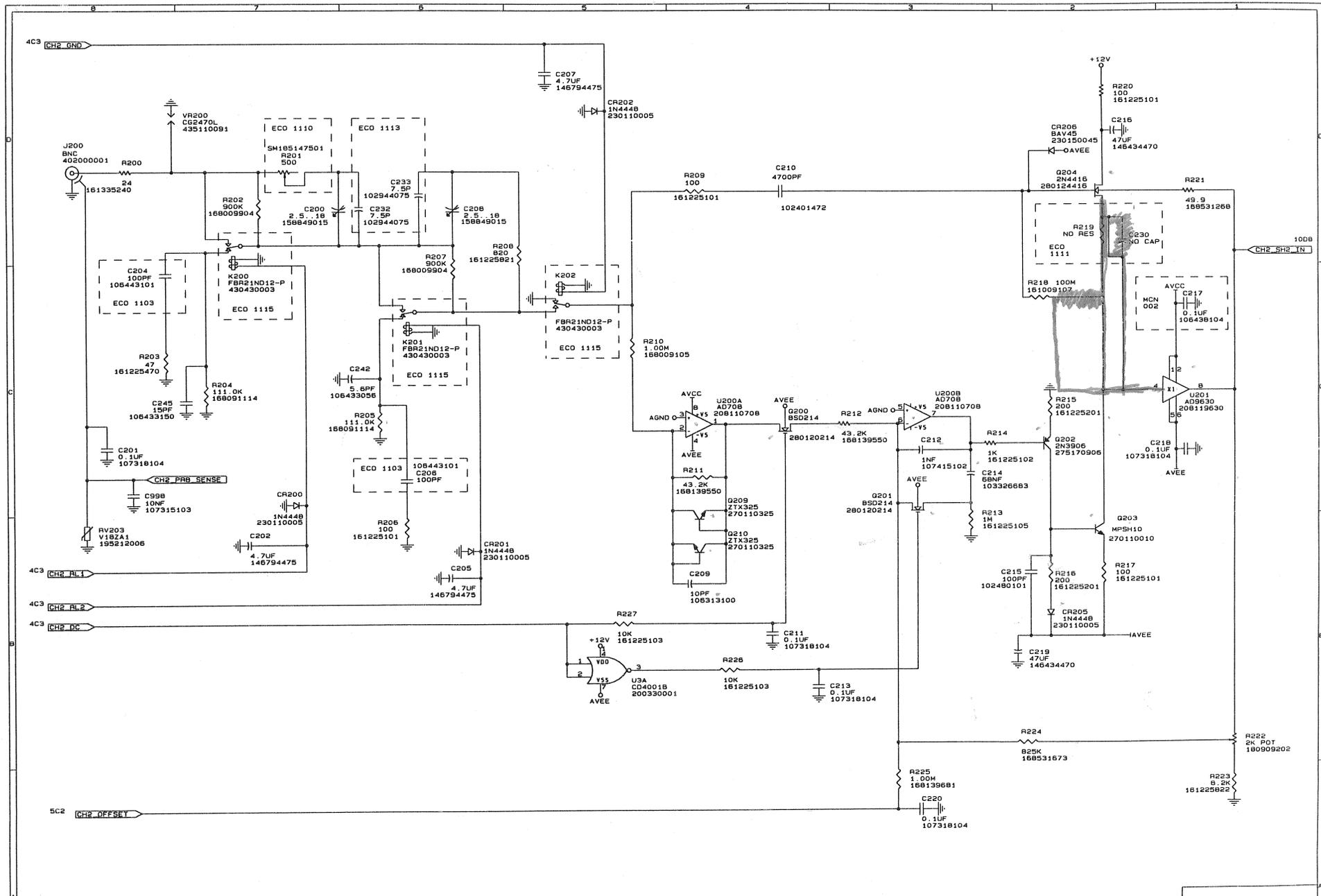
CHANNEL 1 INPUT (HI-Z) STAGE		REV
Size	Document Number	1115
C	SCTFG1_1.SCH	7 of 30
Date:	May 23, 1994	7 of 30



NOBLO	GAIN_0	GAIN_1	GAIN_2	GAIN_3	SELECTION	RANGE
1	0	0	1	1	2.5	100mV 1V 10V
1	0	1	1	0	5	50mV 500mV 5V
0	1	0	0	1	12.5	20mV 200mV 2V
0	1	1	0	0	25	10mV
0	1	1	1	1	1	5mV
1	0	0	0	0		DESELECTED

ECO: 724-F-1/1111
MCN: 724-F-1/002

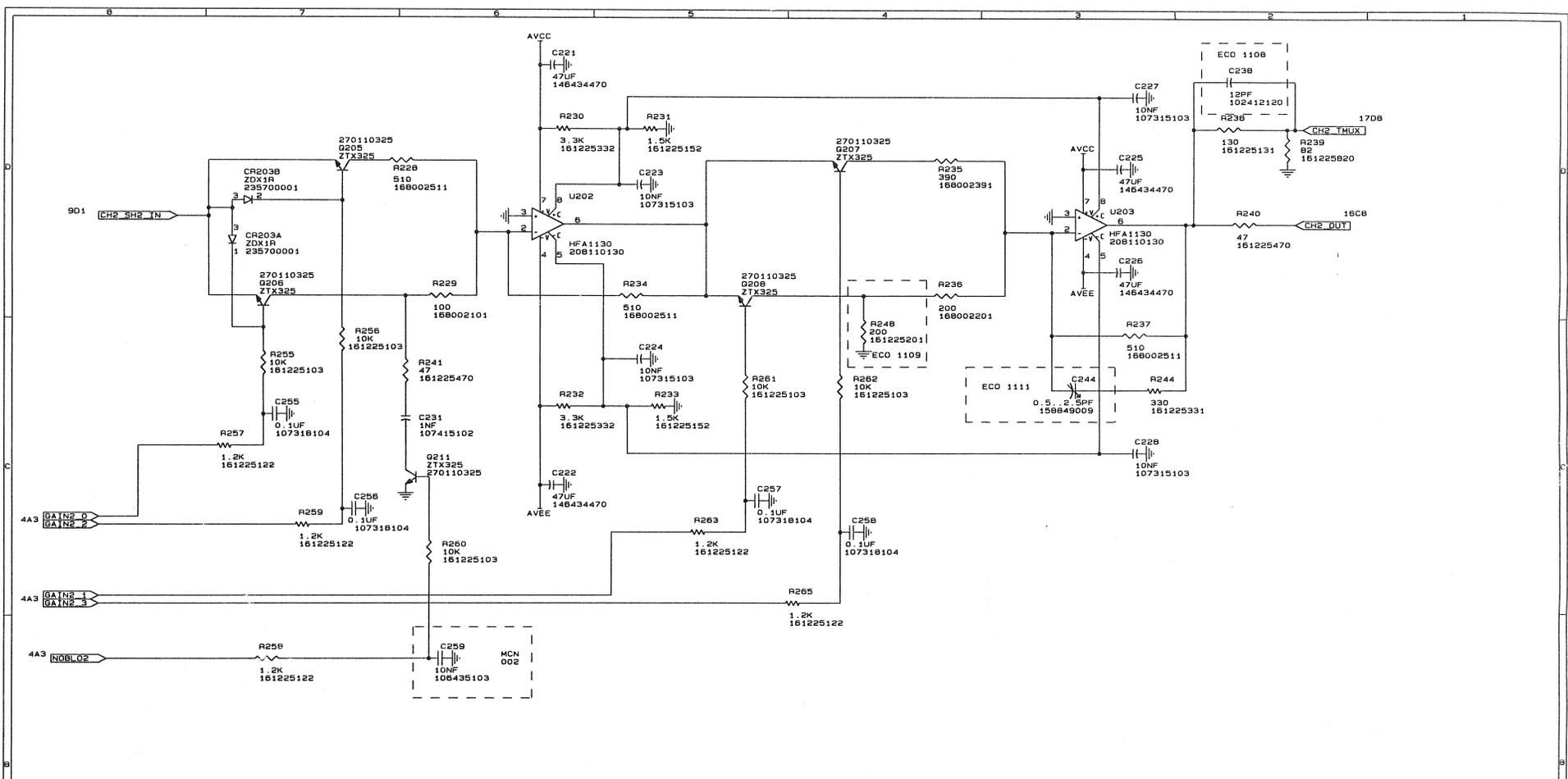
LeCroy Ltd.
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 Title: CHANNEL 1 SECOND STAGE
 Size: Document Number
 C: SCTF61_2_SCH
 Date: May 23, 1994 Sheet 8 of 35



ECO: 724-F-1/1115
 MCN: 724-F-1/002

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Title	CHANNEL 2 INPUT (HI-Z) STAGE	REV
Size	Document Number	B
C	SCTFG2_1 SCH	
Date:	June 13, 1994 Sheet	9 of 35

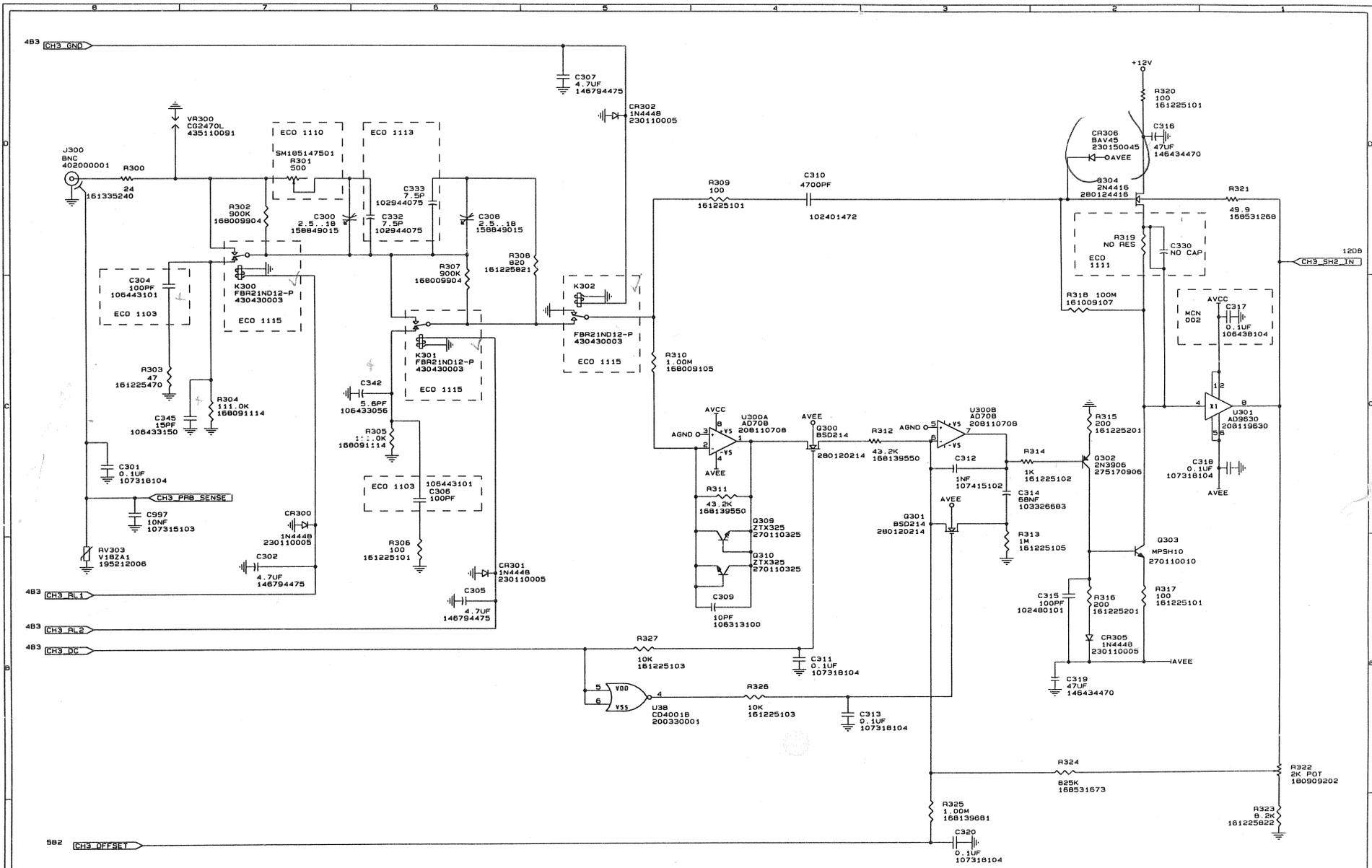


NOBL0	GAIN_0	GAIN_1	GAIN_2	GAIN_3	SELECTION	RANGE
1	0	0	1	1	2.5	100mV 1V 10V
1	0	1	1	0	5	50mV 500mV 5V
0	1	0	1	0	12.5	20mV 200mV 2V
0	1	1	0	0	25	10mV
0	1	1	1	1	1	5mV
1	0	0	0	0		DESELECTED

ECO: 724-F-1/1111
MCN: 724-F-1/002

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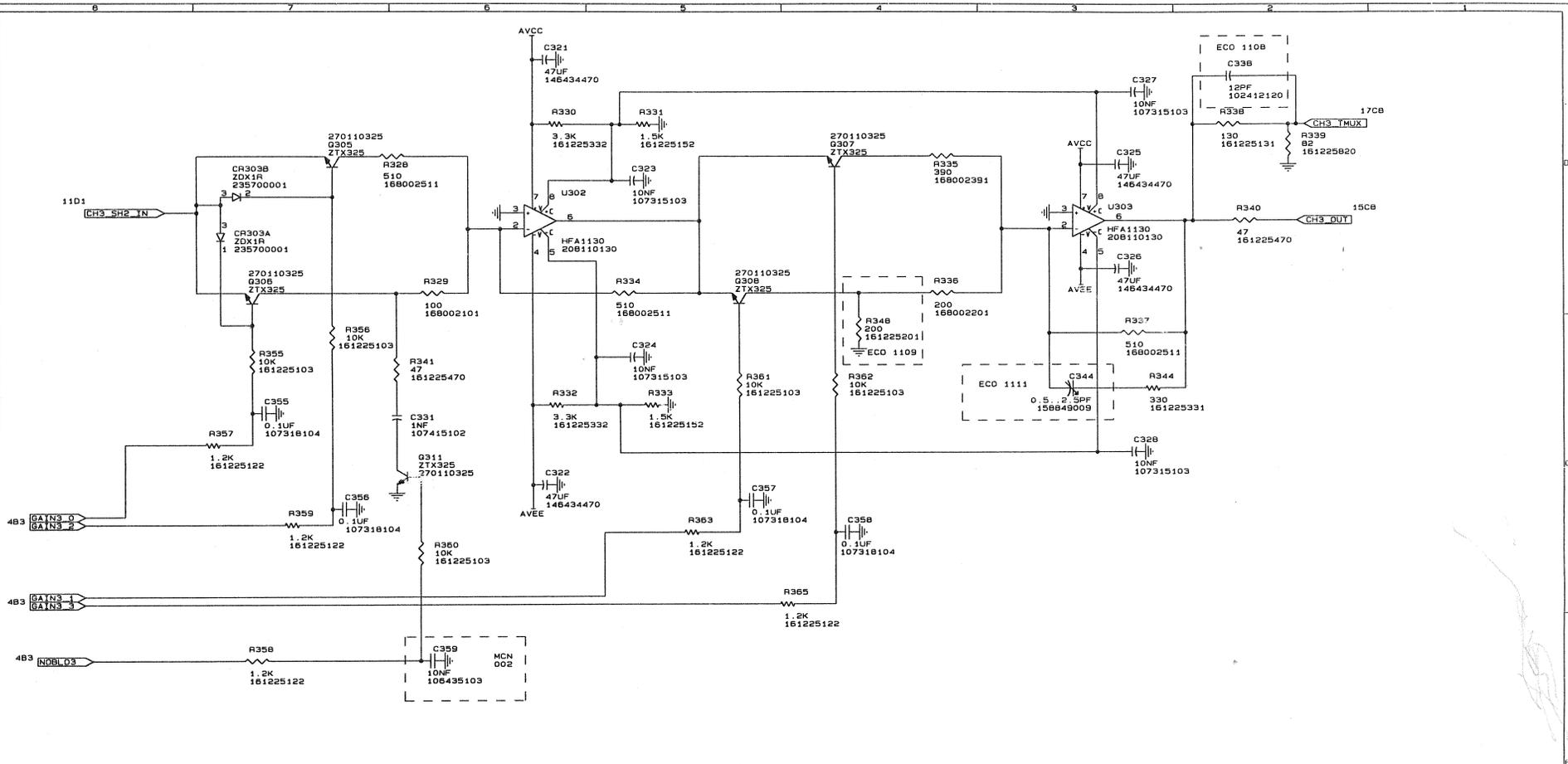
Title	CHANNEL 2 SECOND STAGE
Size	Document Number
C	SCIFG2_2_SCH
Date:	May 23, 1994 Sheet 10 of 35



ECO: 724-F-1/1115
MCN: 724-F-1/002

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Title: CHANNEL 3 INPUT (HI-Z) STAGE
Size: Document Number: REV B
C: SCTFG3_1 SCH
Date: June 13, 1994 Sheet 11 of 35

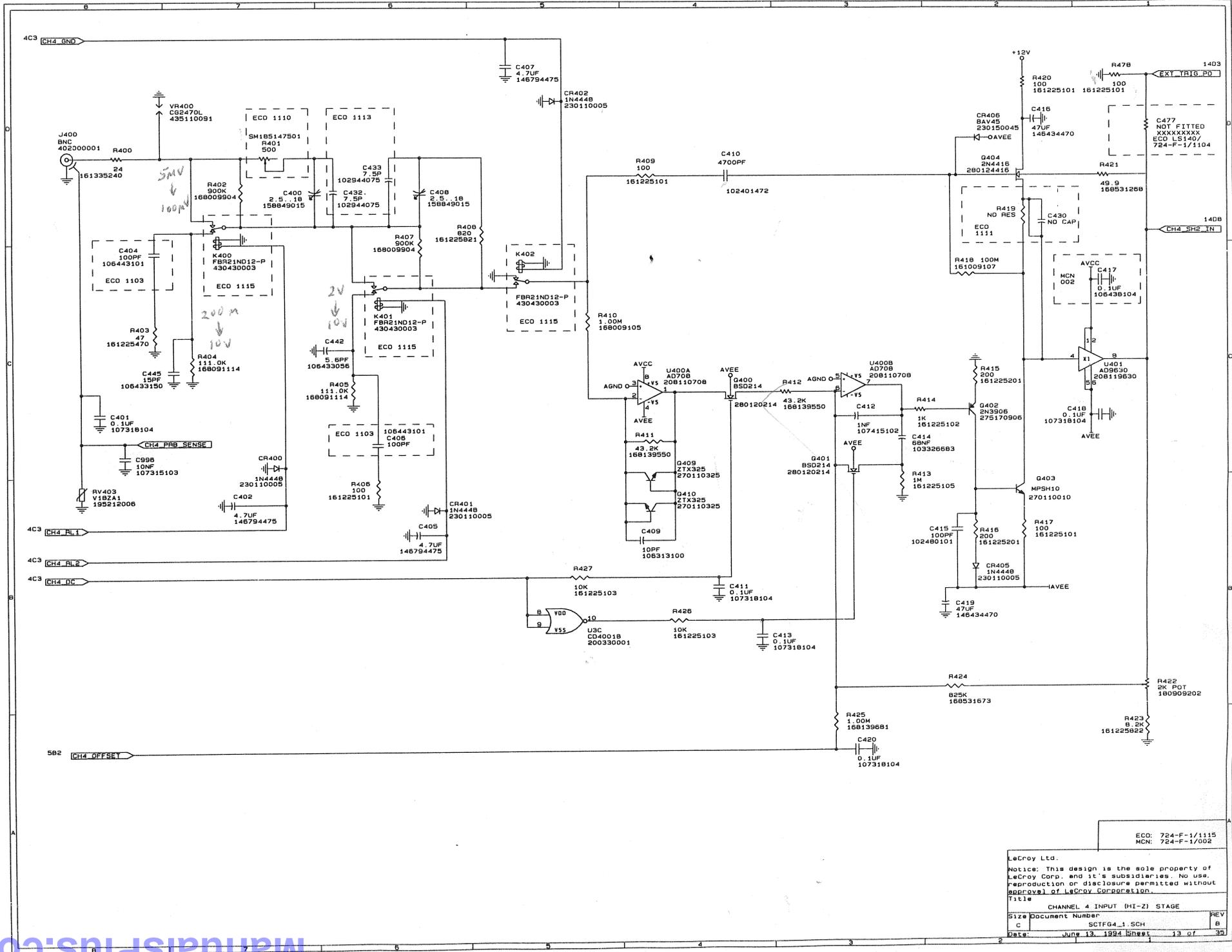


NOBL_0	GAIN_0	GAIN_1	GAIN_2	GAIN_3	SELECTION	RANGE
1	0	0	1	1	2.5	100mV 1V 10V
1	0	1	1	0	5	50mV 500mV 5V
0	1	0	0	1	12.5	20mV 200mV 2V
0	1	1	0	0	25	10mV
0	1	1	1	1	1	5mV
1	0	0	0	0		DESELECTED

ECO: 724-F-1/1111
MCN: 724-F-1/002

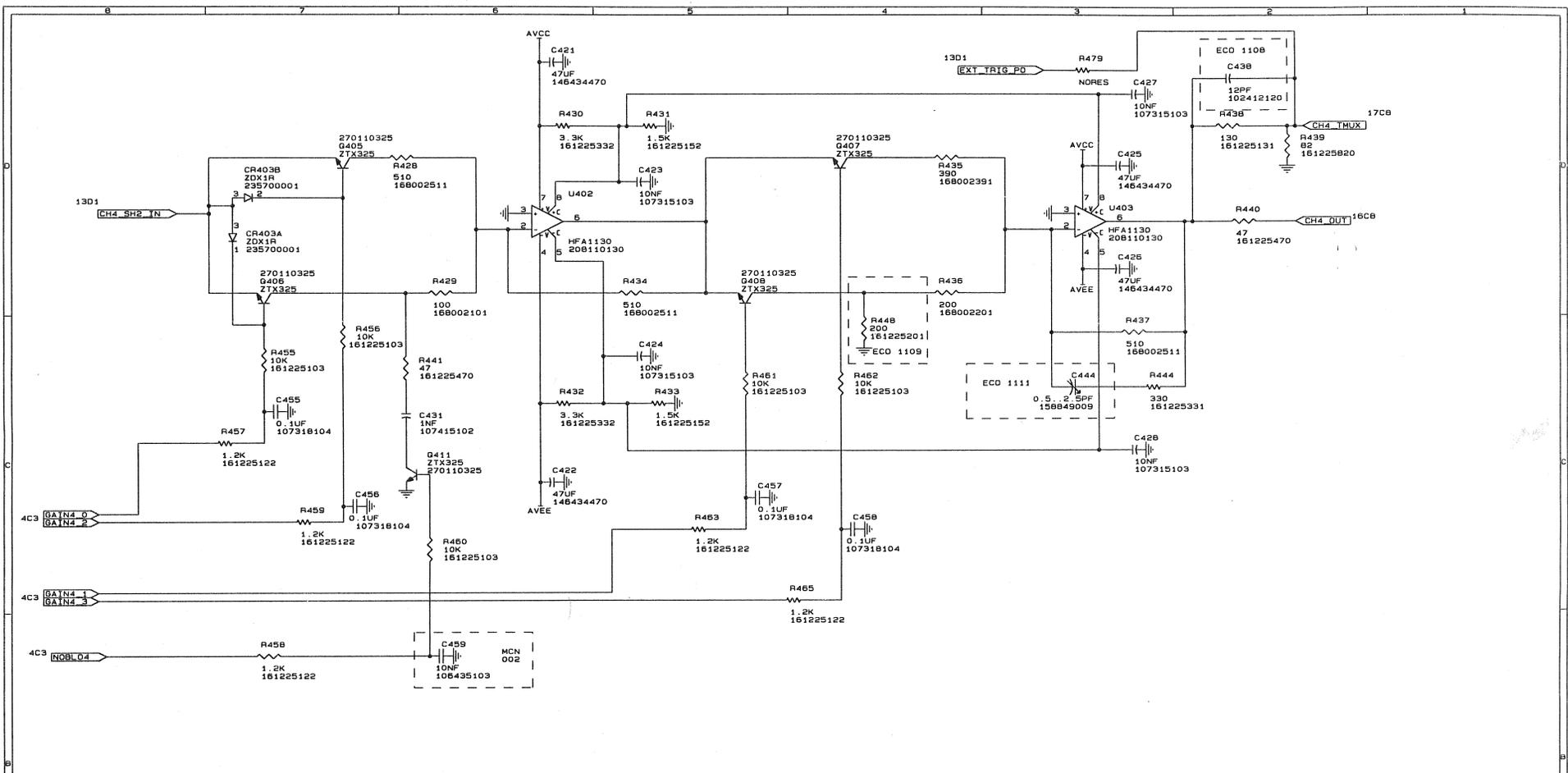
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Title: CHANNEL 3 SECOND STAGE
Size: Document Number: SC1F03_2 SCH
Date: May 23, 1994 Sheet 12 of 35



ECO: 724-F-1/1115
MCN: 724-F-1/002

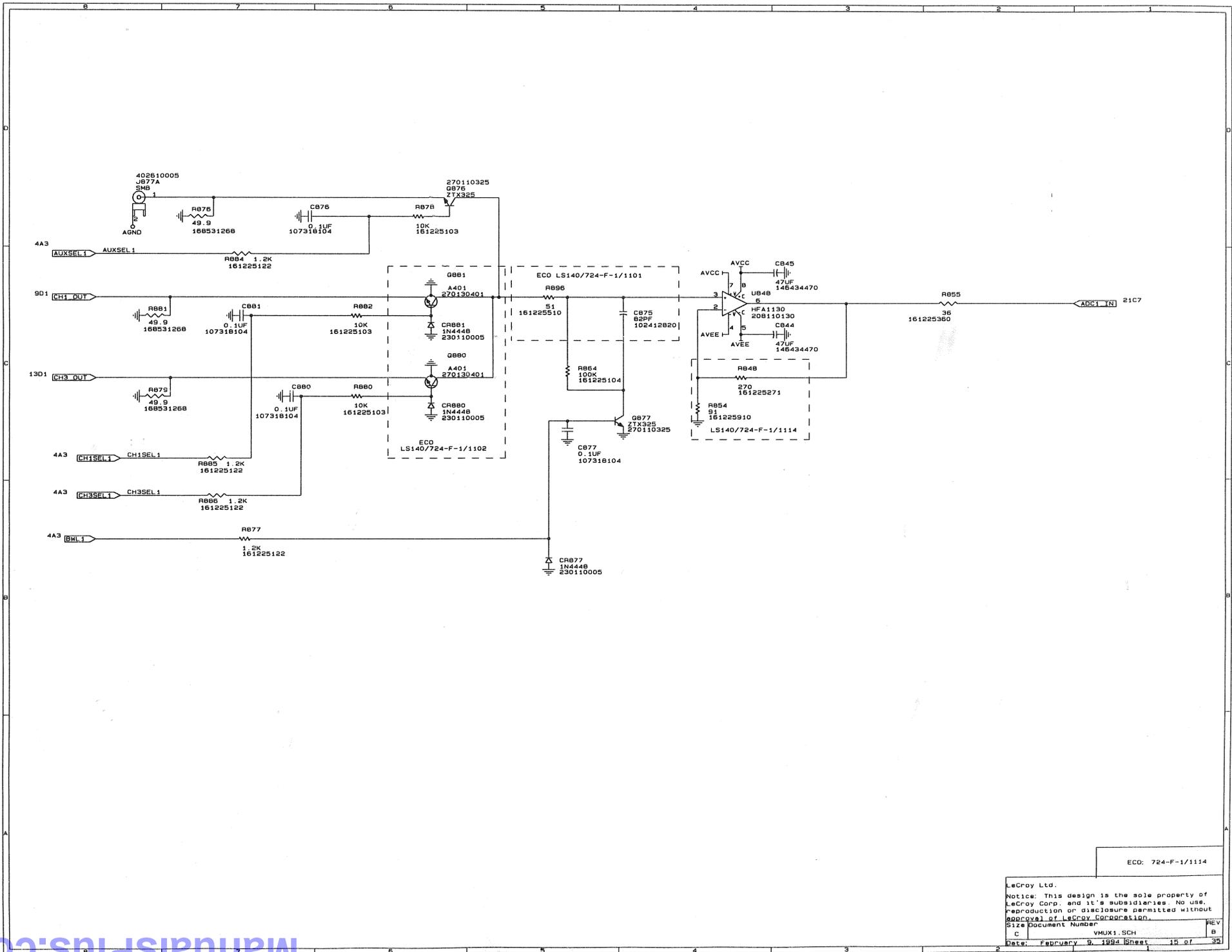
LeCroy Ltd.
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 Title: CHANNEL 4 INPUT (HI-Z) STAGE
 Size: Document Number: SCTFG4_1_SCH
 Date: June 13, 1994 Sheet: 13 of 30



NOBLO	GAIN_0	GAIN_1	GAIN_2	GAIN_3	SELECTION	RANGE
1	0	0	1	1	2.5	100mV 1V 10V
1	0	1	1	0	5	50mV 500mV 5V
0	0	0	0	1	12.5	20mV 200mV 2V
0	1	1	0	0	25	10mV
0	1	1	1	1	1	5mV
1	0	0	0	0		DESELECTED

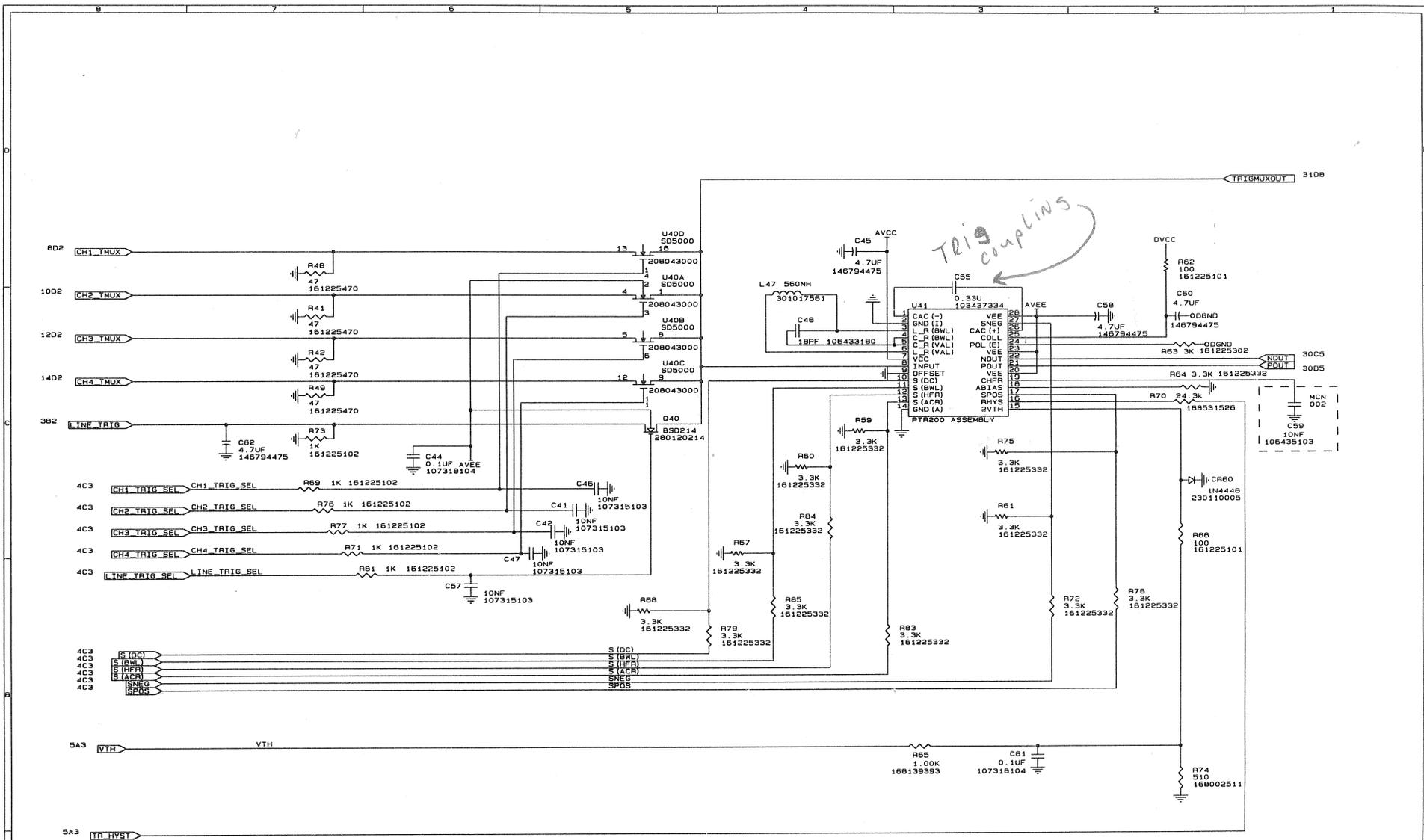
ECO: 724-F-1/1111
MCN: 724-F-1/002

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Title: CHANNEL 4 SECOND STAGE
Size: Document Number: SCTFG4_2_SCH
C: SCTFG4_2_SCH
Date: May 23, 1994 Sheet 14 of 35



ECO: 724-F-1/1114

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 Size Document Number
 C VMUX1.SCH
 Date: February 9, 1994 Sheet 15 of 35



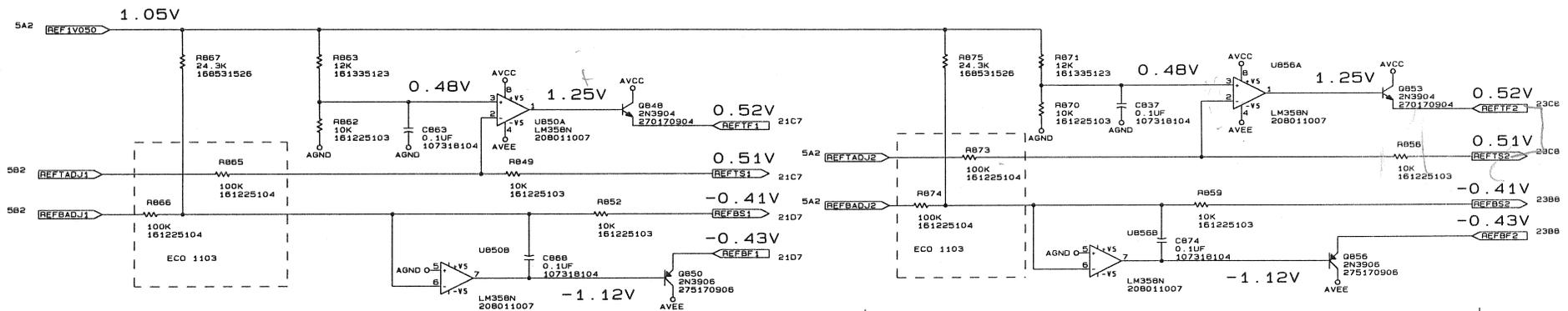
CONTROL LINE

MODE	DC	BWL	HFR	ACR
DC	1	0	0	0
DC, BWL	0	1	0	0
AC	1	0	0	1
AC, BWL	0	1	0	1
LFR	1	0	1	0
LFR, BWL	0	1	1	0
HFR	0	0	1	0

ECO: 724-F-1/1106
MCN: 724-F-1/002

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Title TRIGGER MUX & DISCRIMINATOR	
Size Document Number	REV
C TMUX SCH	B
Date: May 23, 1994	Sheet 17 of 35

VOLTAGES SHOWN ARE NOT PRECISE.
 THEY WILL CHANGE AT DIFFERENT VOLTS/DIV SETTINGS.
 THEY WILL BE DIFFERENT BETWEEN INSTRUMENTS.



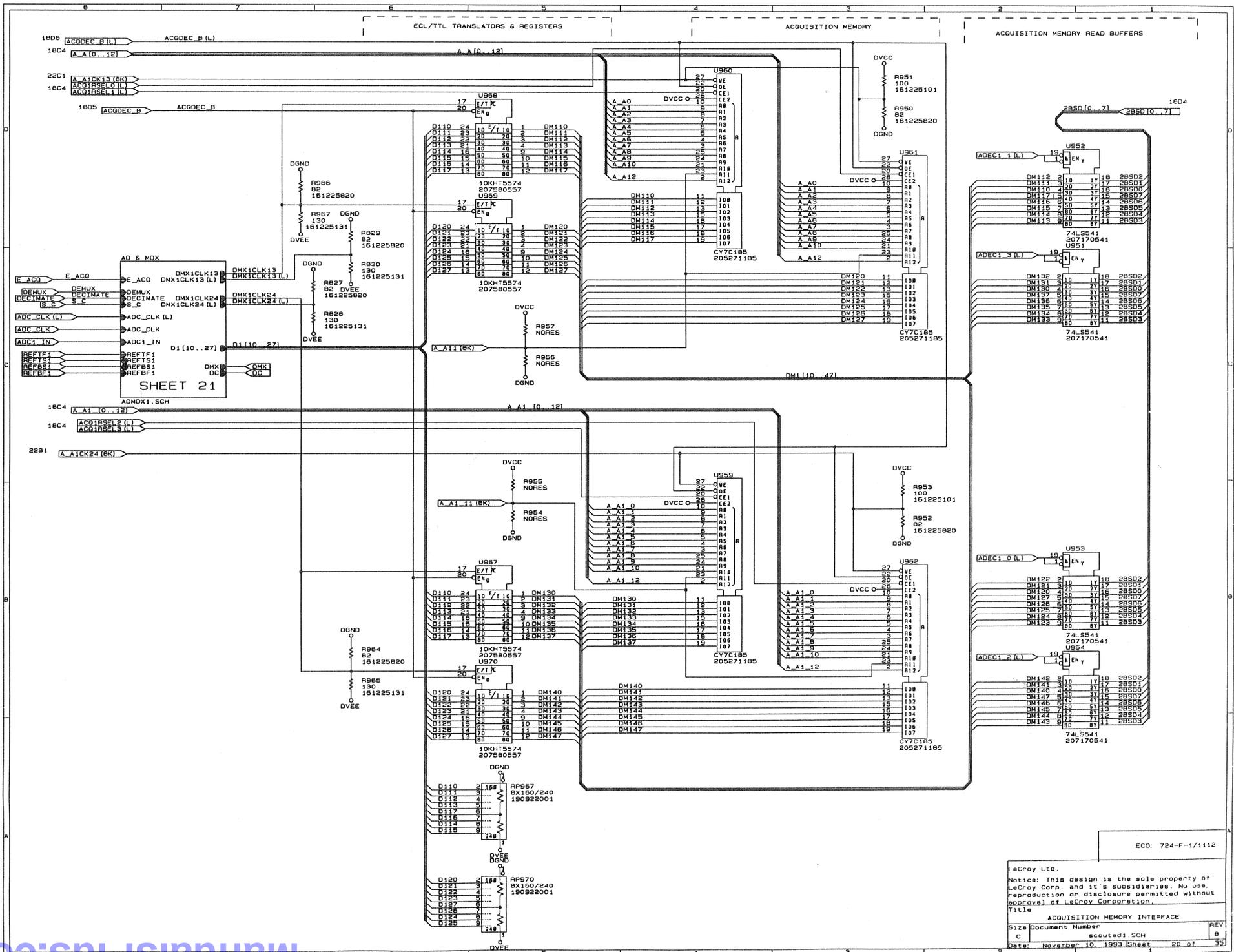
----- REFERENCE VOLTAGES FOR U849 -----

----- REFERENCE VOLTAGES FOR U854 -----

ECO: 724-F-1/1106

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Title		ADC REFERENCE ADJUSTORS	
Size	Document Number	REV	B
C	adcref.sch		
Date:	September 8, 1993	Sheet	19 of 32

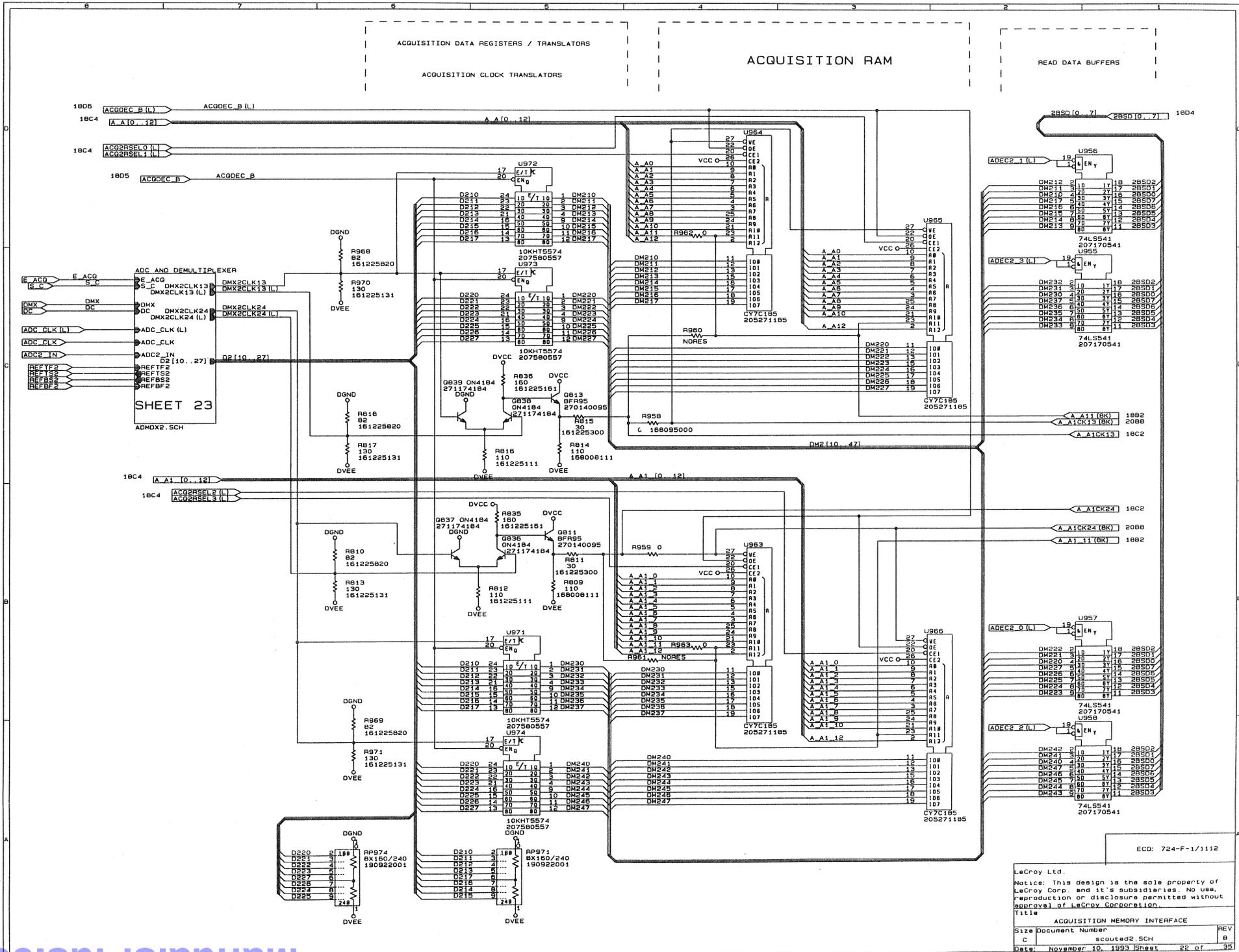


SHEET 21

ECO: 724-F-1/1112

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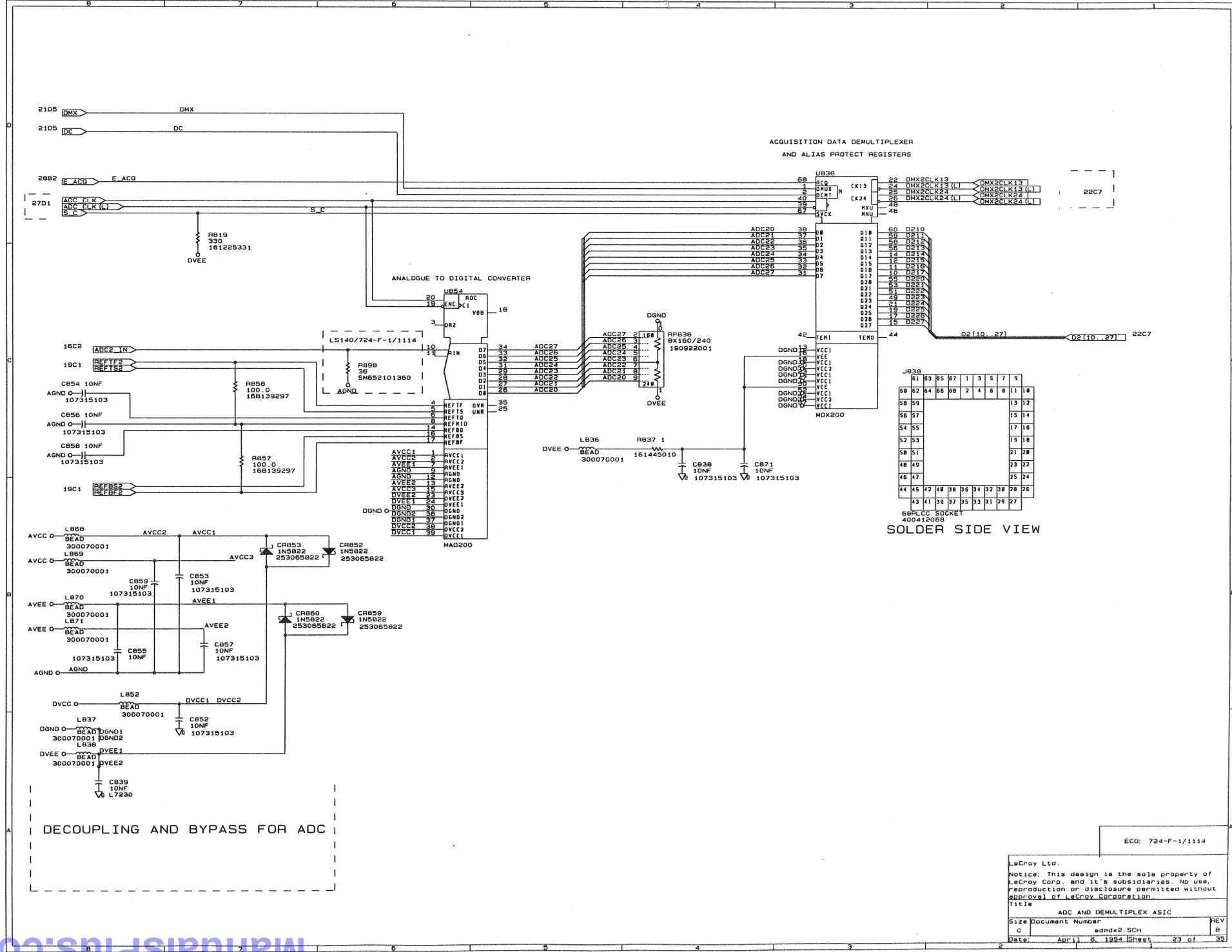
Title: ACQUISITION MEMORY INTERFACE
 Size: Document Number
 C: acouted1 SCH
 Date: November 10, 1993 Sheet 20 of 35



ECO: 724-F-1/1112

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 Title: ACQUISITION MEMORY INTERFACE

Size Document Number: scoutas2 SCH
 Date: November 10, 1993 Sheet: 22 of 25

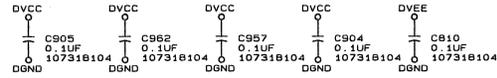
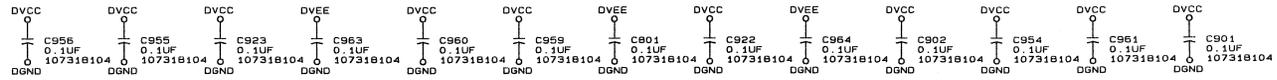


DECOUPLING AND BYPASS FOR ADC

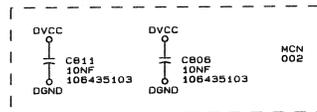
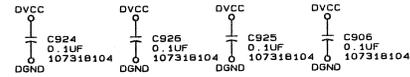
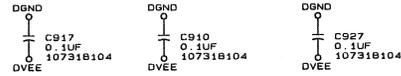
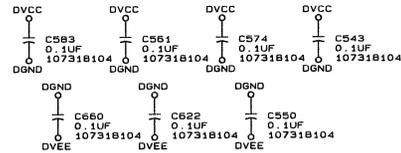
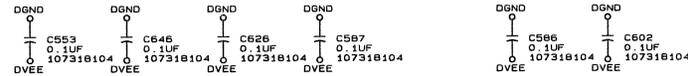
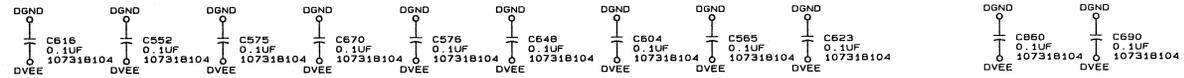
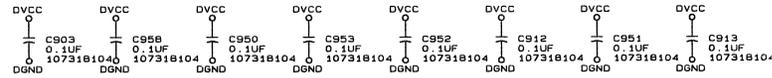
SOLDER SIDE VIEW

ECO: 724-F-1/1114

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 Title: ADC AND DEMULTIPLEXER ASIC
 Size: Document Number: admx2.SCH REV: B
 Date: April 8, 1994 Sheet: 23 of 35



DVCC-DVCC



ECD: 724-F-1/1106
MCN: 724-F-1/002

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Title: ACQUISITION AND TRIGGER BYPASS CAPACITORS
Size: Document Number
C AC00CPL.SCH
REV: B

Date: May 23, 1994 Sheet 24 of 35

25D6 BSD0[0..7] BSD1[0..7]

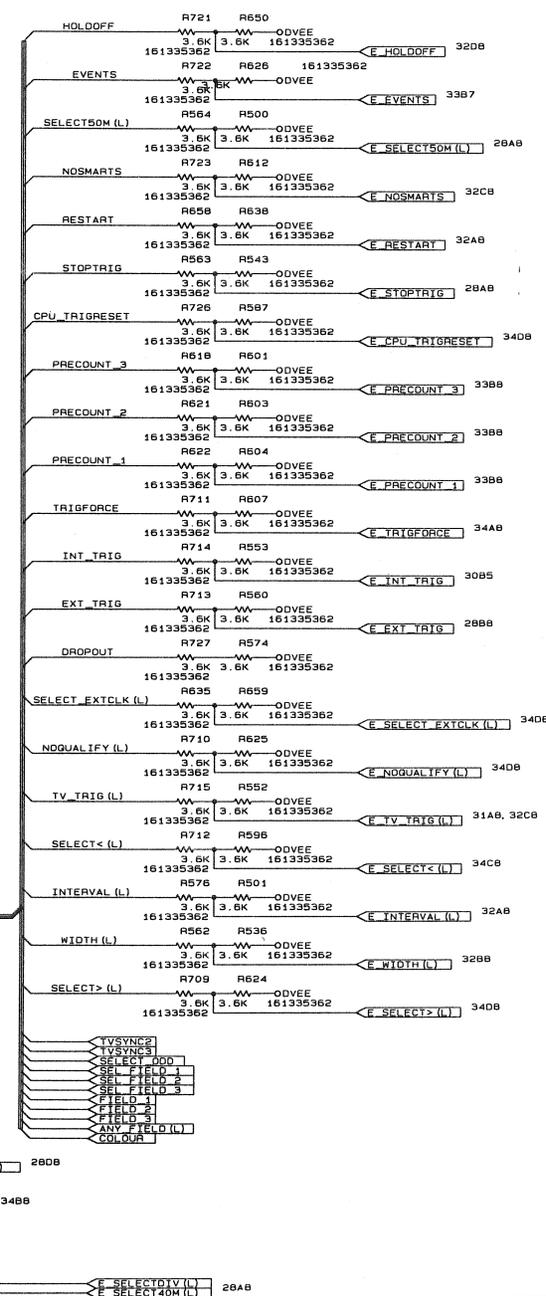
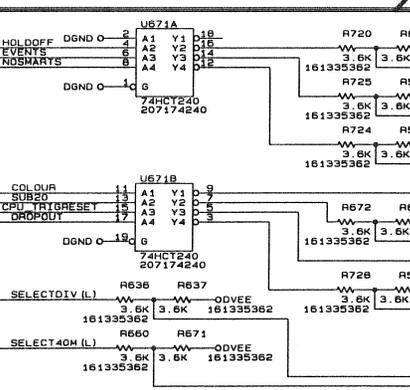
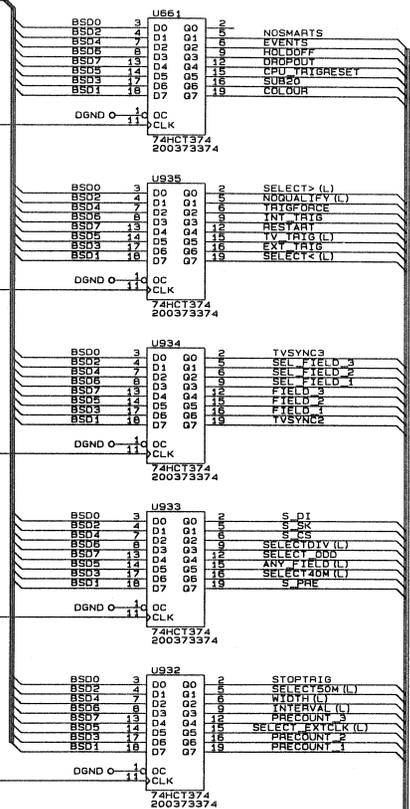
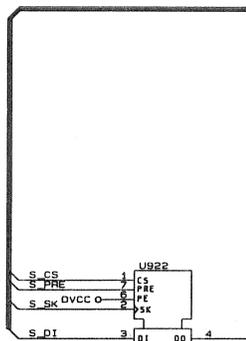
25D8 TRG_FCNI(L)

25D8 TRG_FCNI(L)

25D8 TRG_FCNI(L)

25D8 TRG_FCNI(L)

25D8 TRG_FCNI(L)

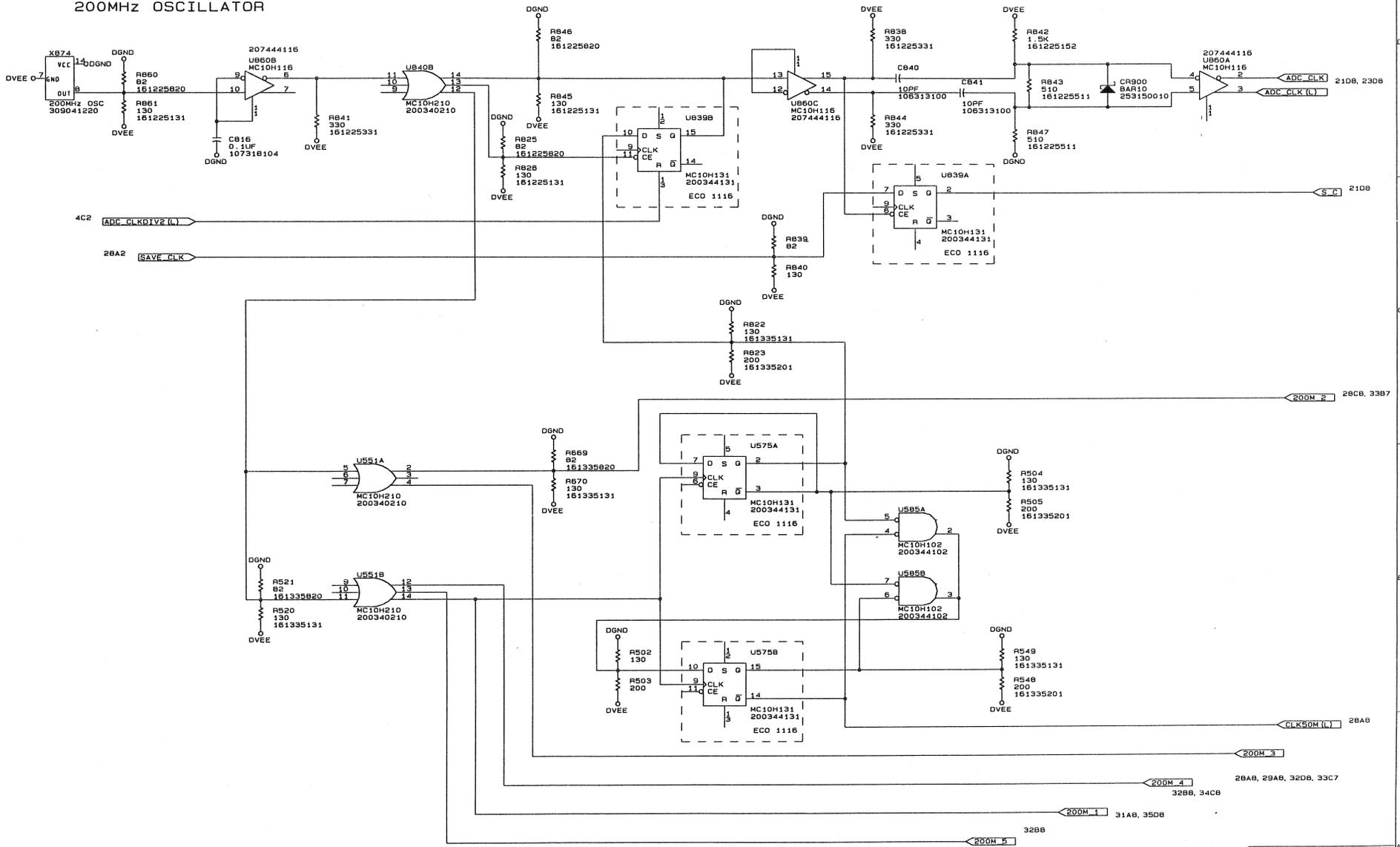


ECO: 724-F-1/1106

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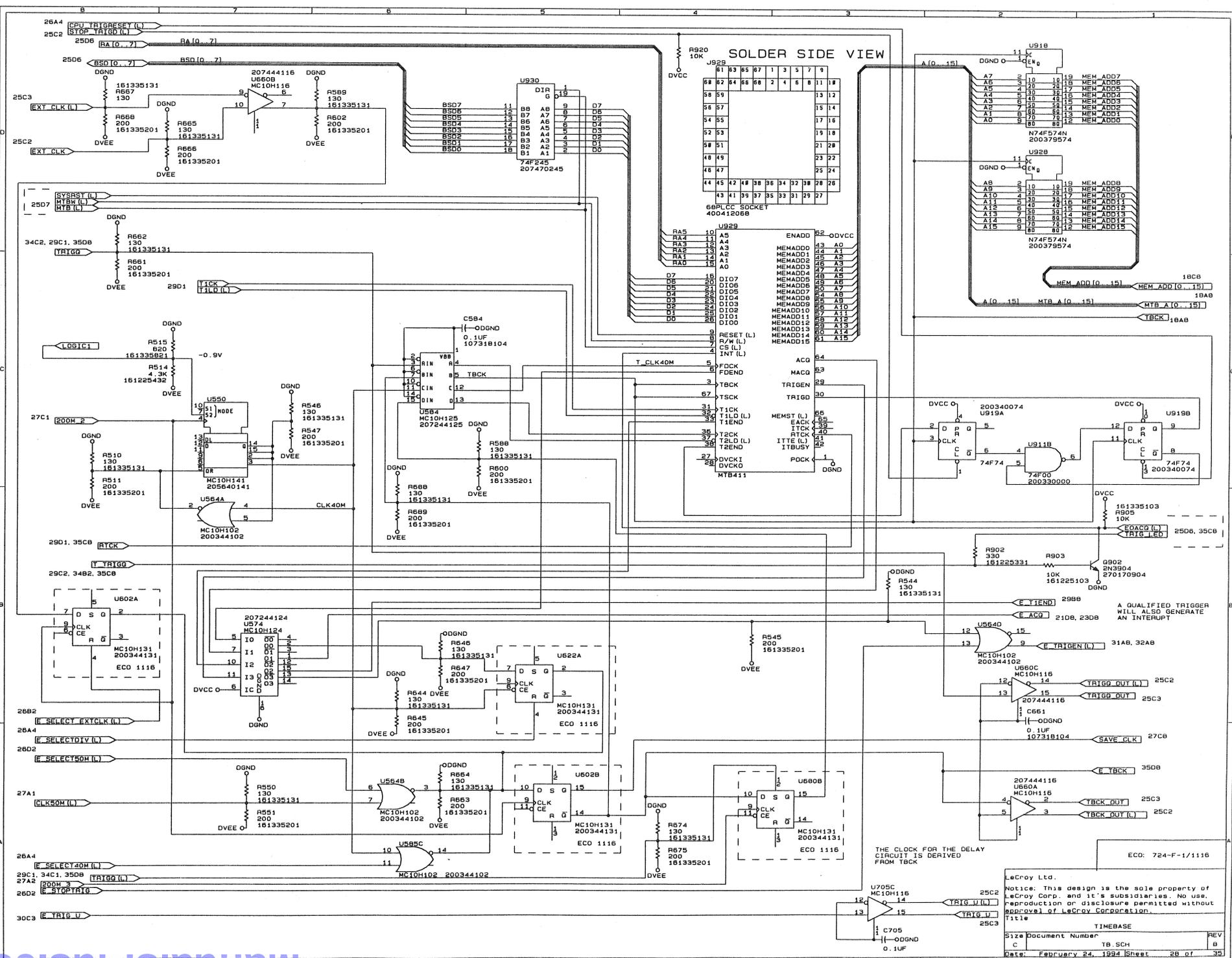
Title		CONTROL
Size	Document Number	REV
C	CONTROL SCH	B
Date:	September 5, 1993	Sheet 28 of 35

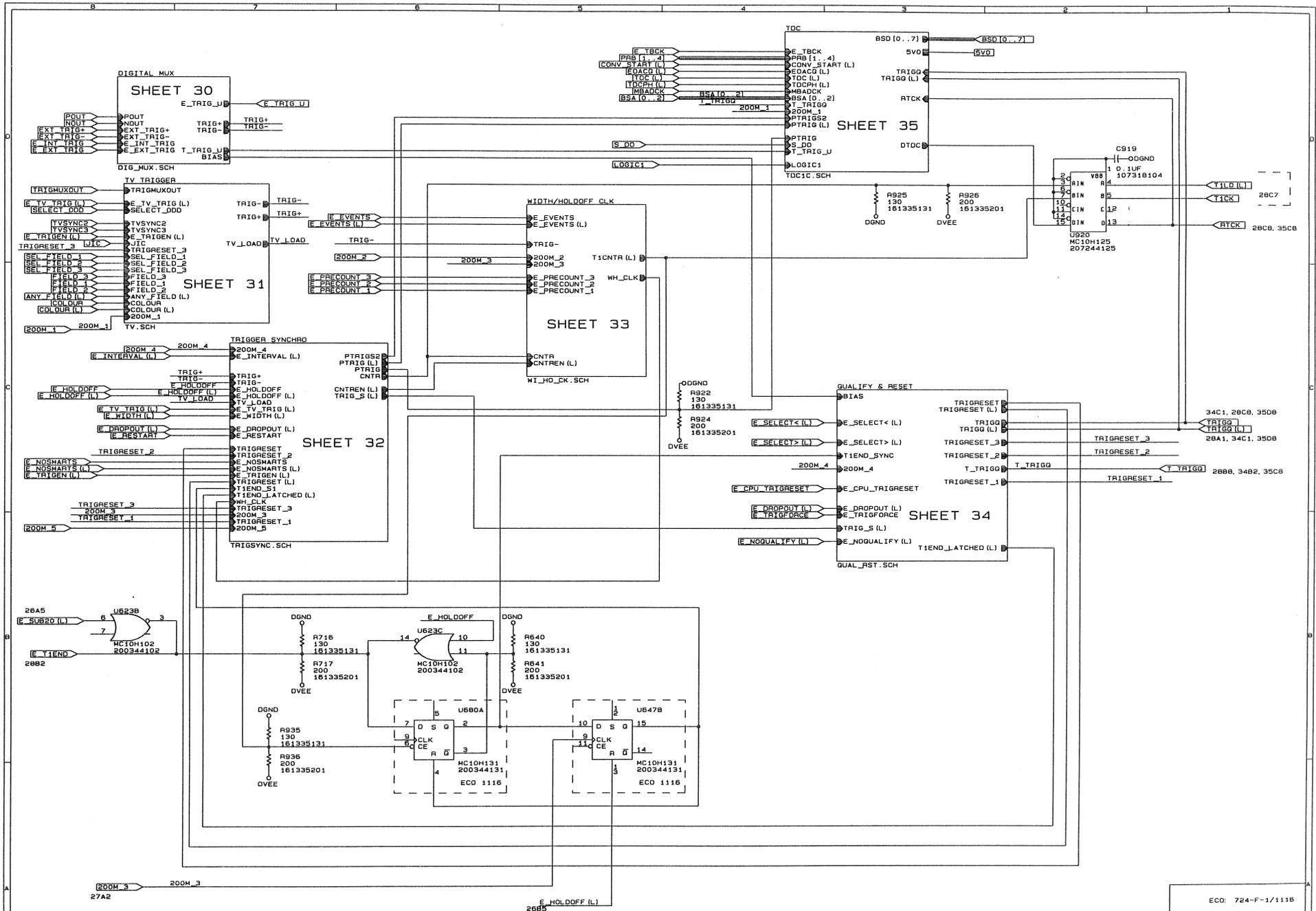
200MHZ OSCILLATOR



ECO: 724-F-1/1116

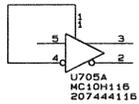
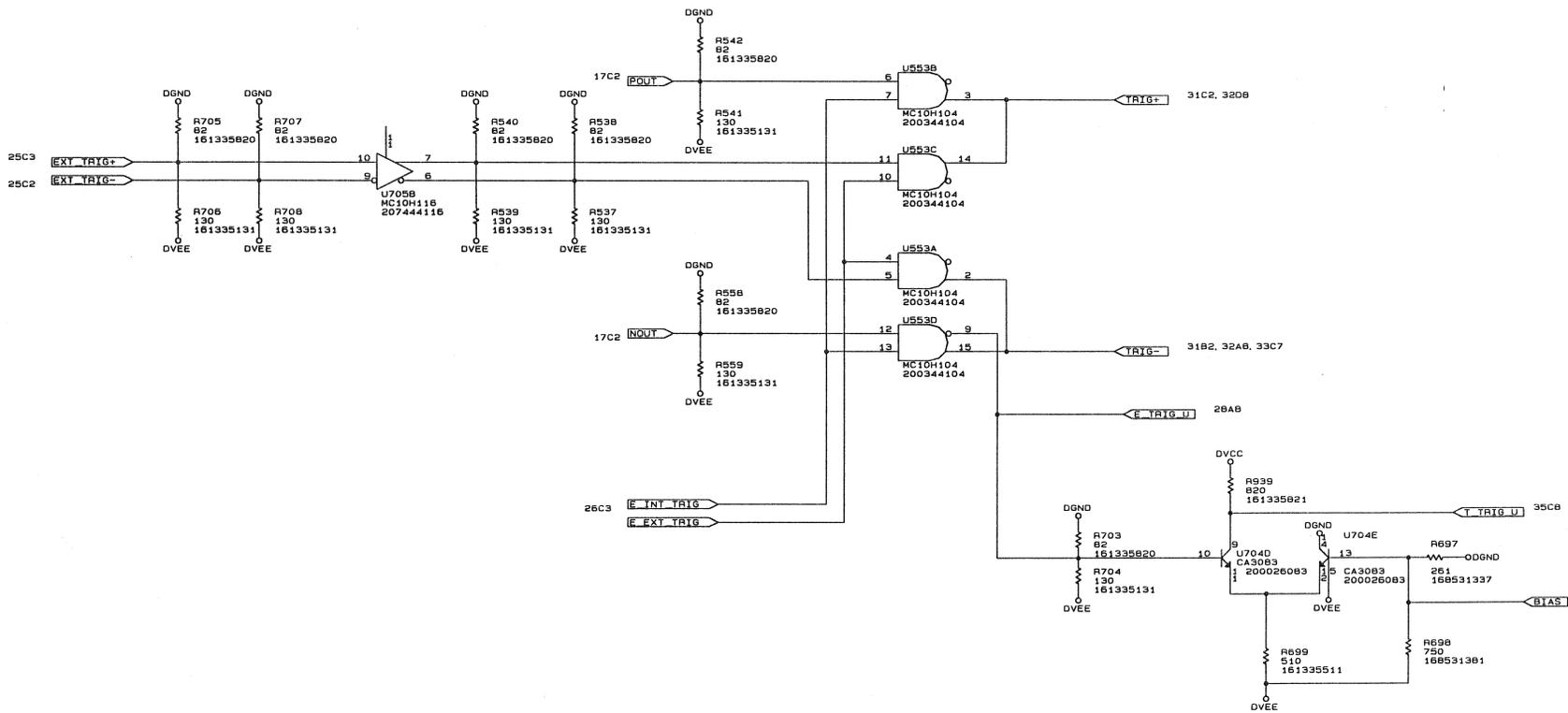
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 Title: CLOCK SOURCE AND DISTRIBUTION
 Size: Document Number: REV B
 C CLK_SRC SCH
 Date: February 24, 1994 Sheet 27 of 30





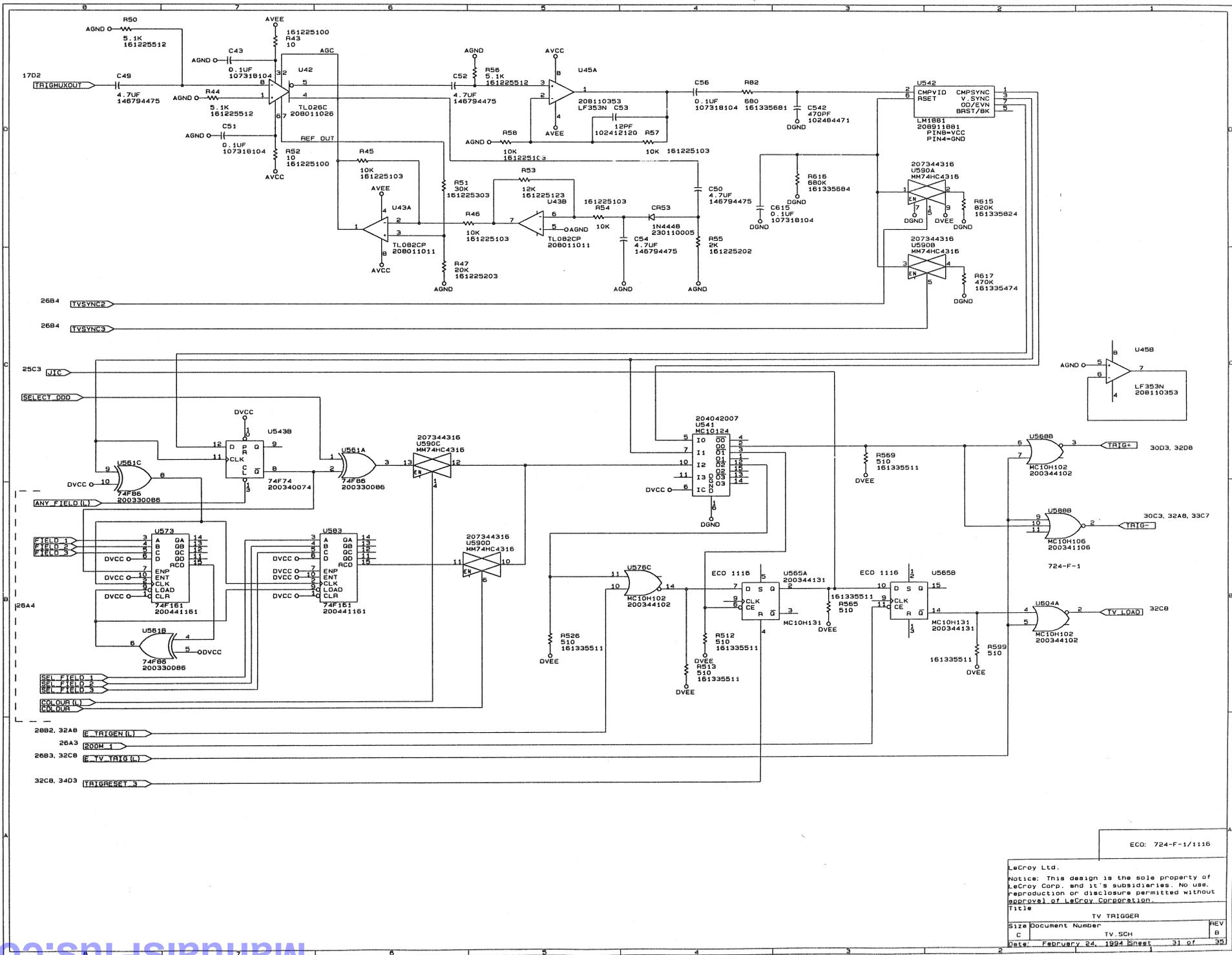
ECO: 724-F-1/1116

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 Title: DIGITAL TRIGGER
 Size Document Number: REV B
 C DIG_TRIG SCH
 Date: February 24, 1994 Sheet 29 of 35



ECO: 724-F-1/1106

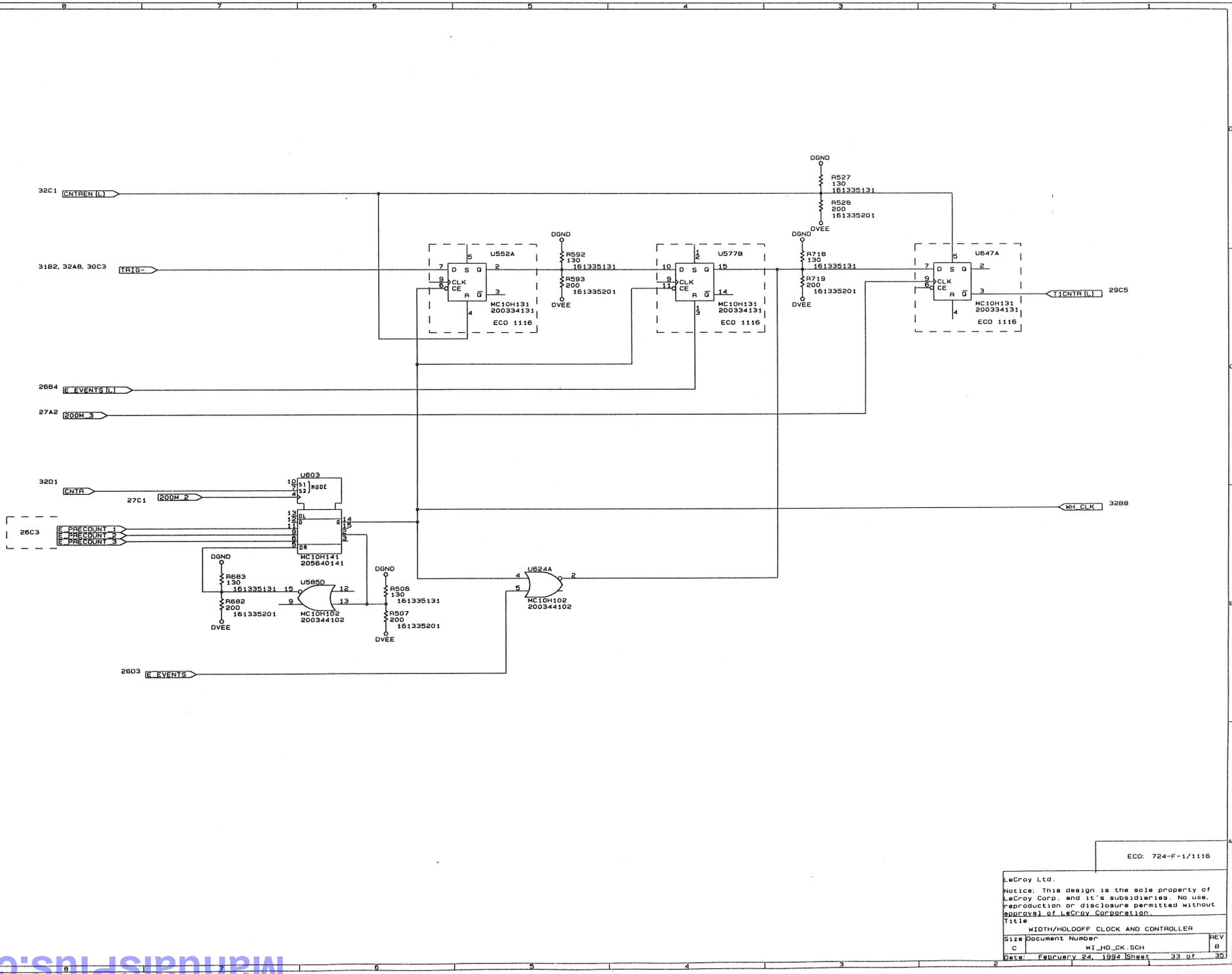
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 Title
 DIGITAL MUX
 Size Document Number
 DIG_MUX_SCH
 Date: September 8, 1981 Sheet 30 of 35



ECO: 724-F-1/1116

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 Title: TV TRIGGER

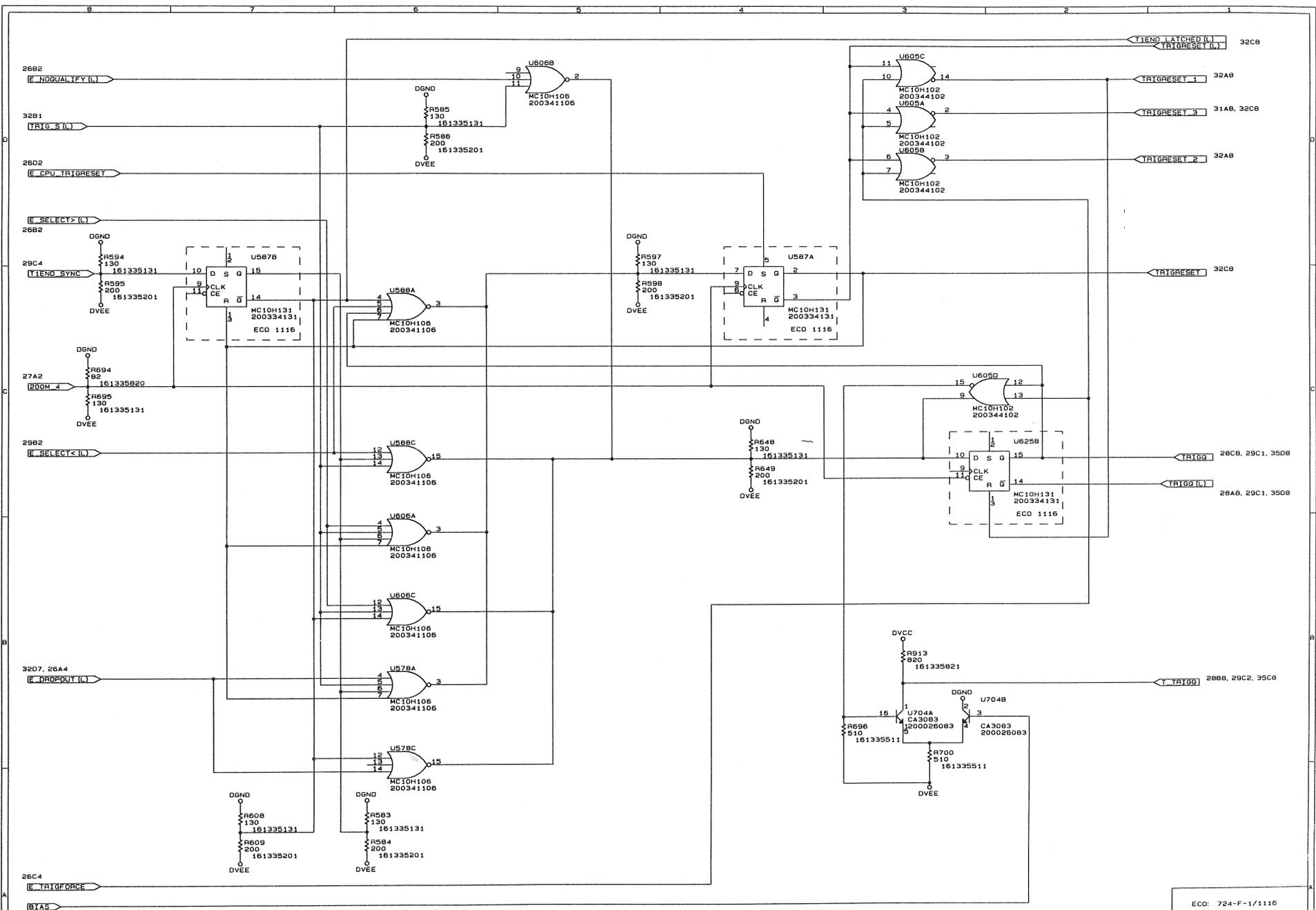
Size	Document Number	REV
C	TV SCH	B
Date:	February 24, 1994	Sheet 31 of 32



ECD: 724-F-1/1116

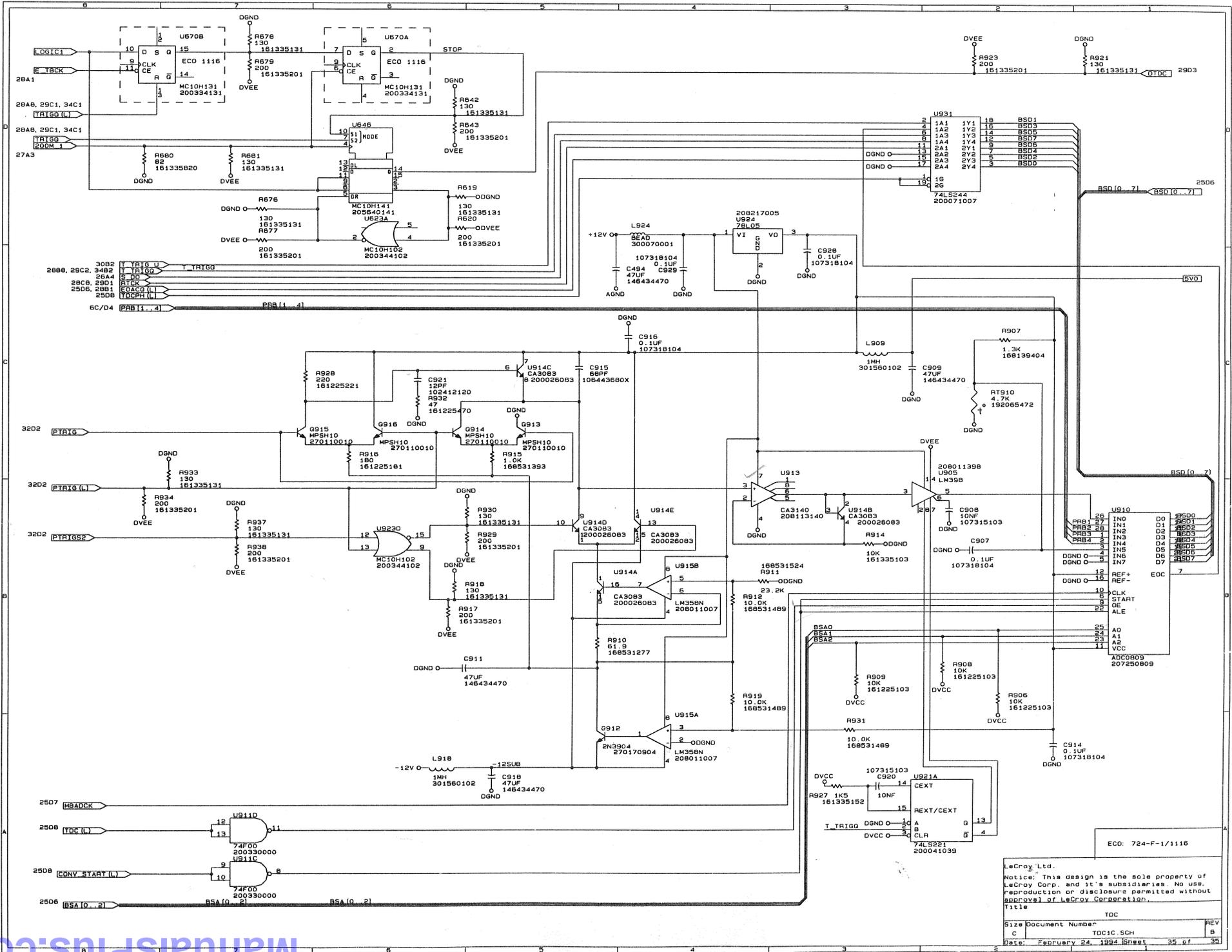
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Title		REV
WIDTH/HOLDOFF CLOCK AND CONTROLLER		0
Size	Document Number	
C	WI_HO_CLK_SCH	
Date:	February 24, 1984	Sheet 33 of 35



ECO: 724-F-1/1116

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 Title: QUALIFY & RESET
 Size: Document Number: REV B
 C: QUAL_RST_SCH
 Date: February 24, 1994 Sheet 34 of 35



ECO: 724-F-1/1116

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 Title _____

Size	Document Number	REV
C	TDC1C.SCH	B
Date:	February 24, 1994	Sheet 35 of 35