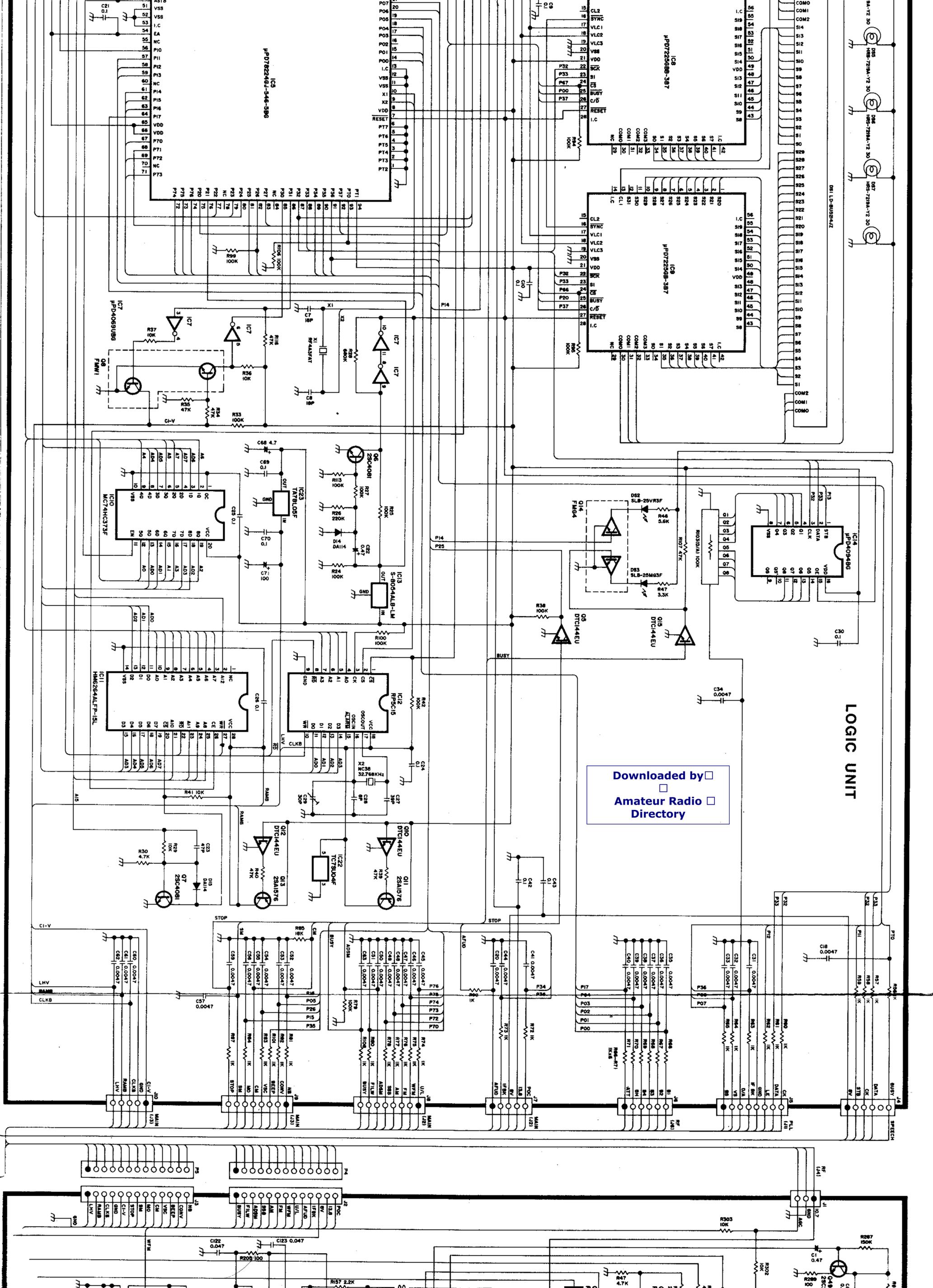
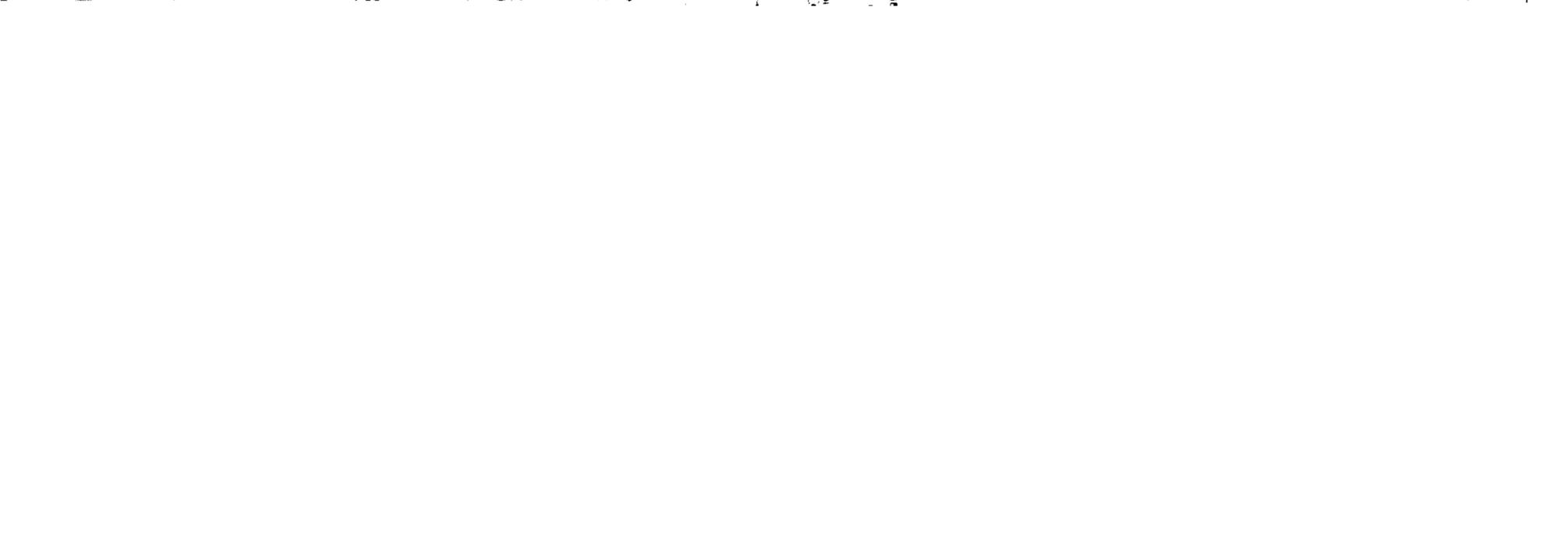
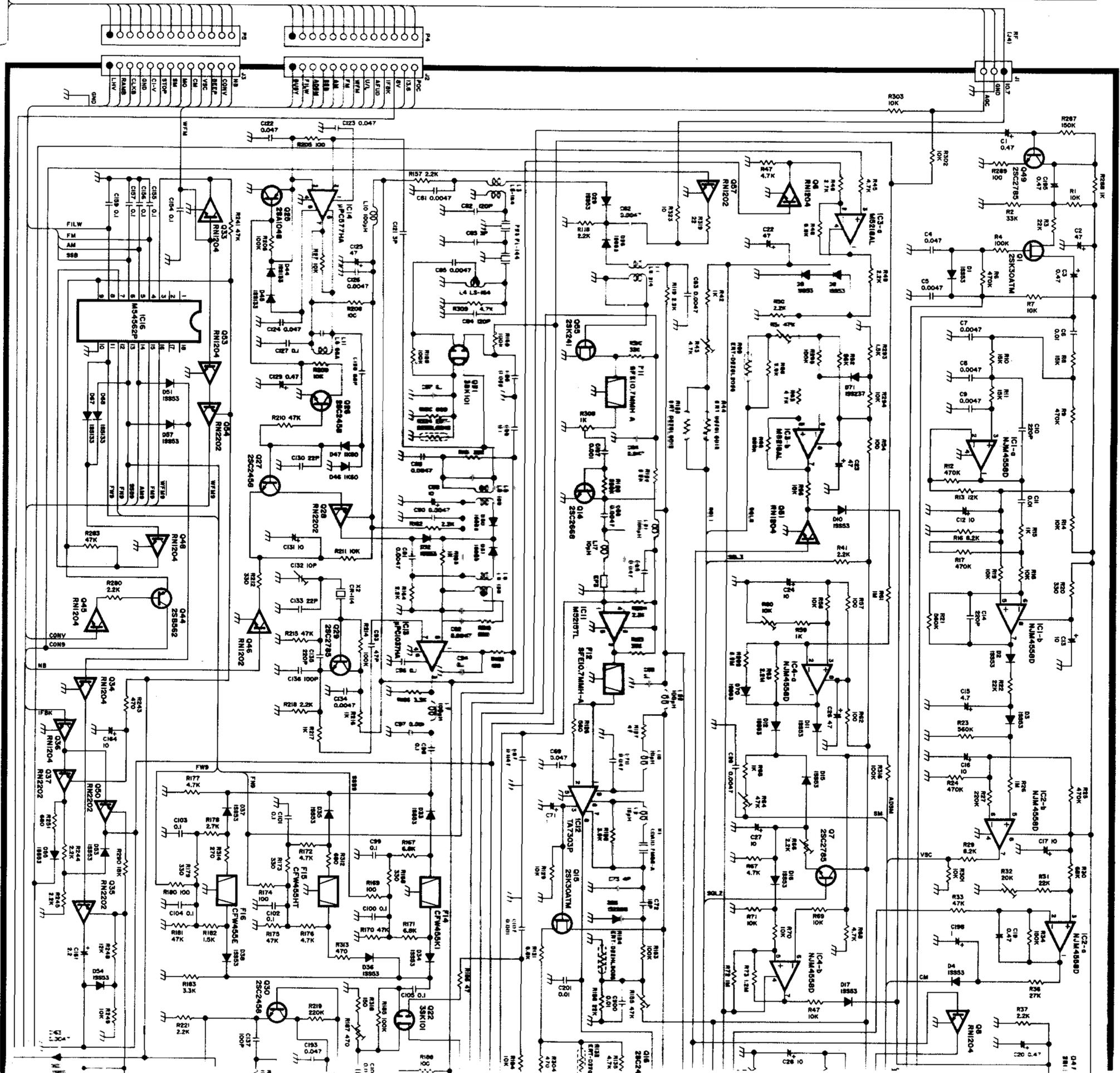
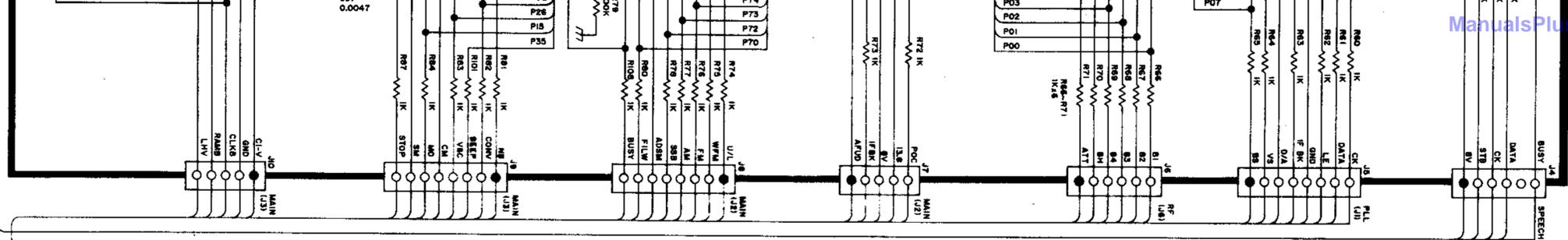


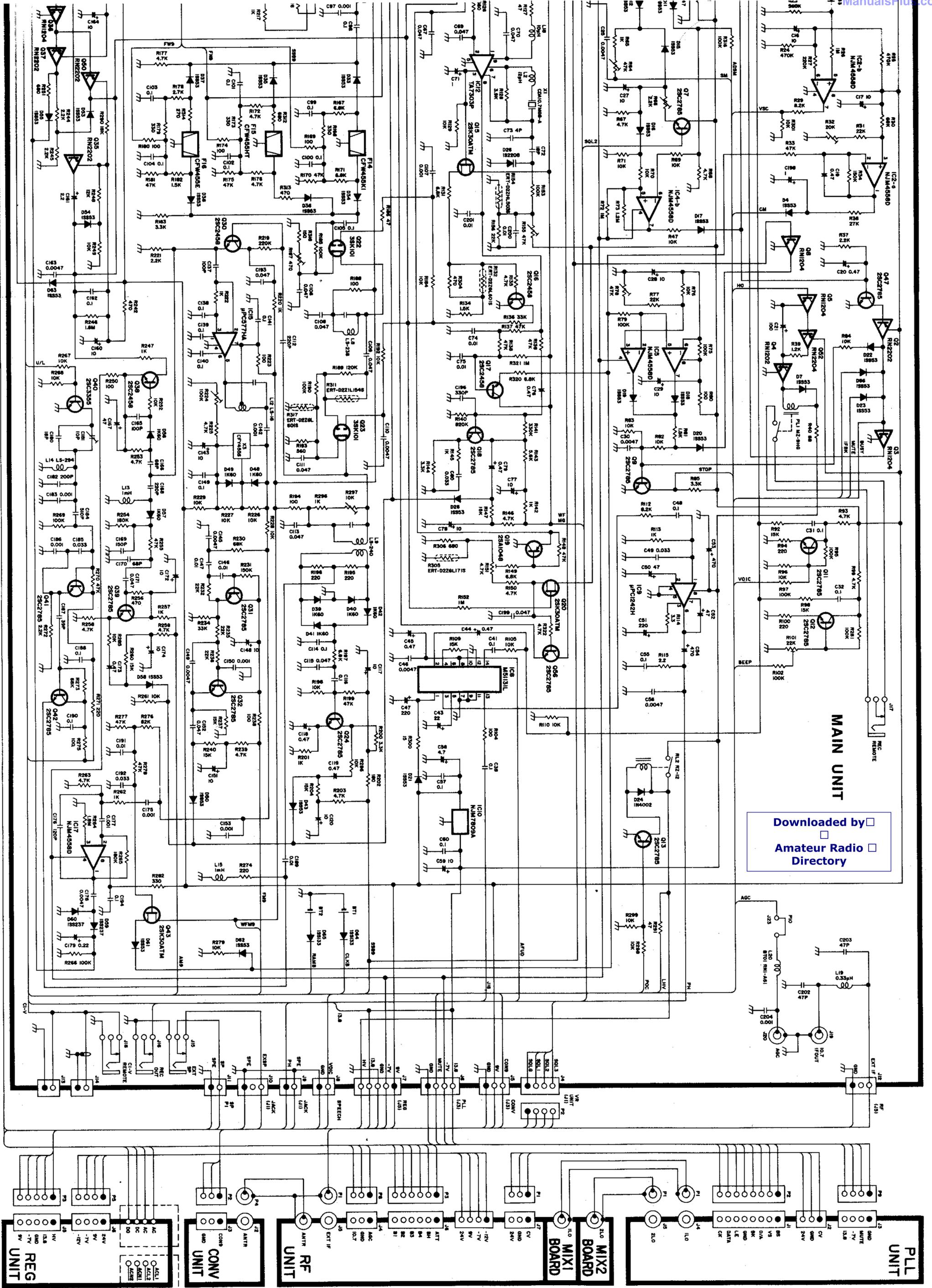


LOGIC UNIT

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MAIN UNIT

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PLL UNIT

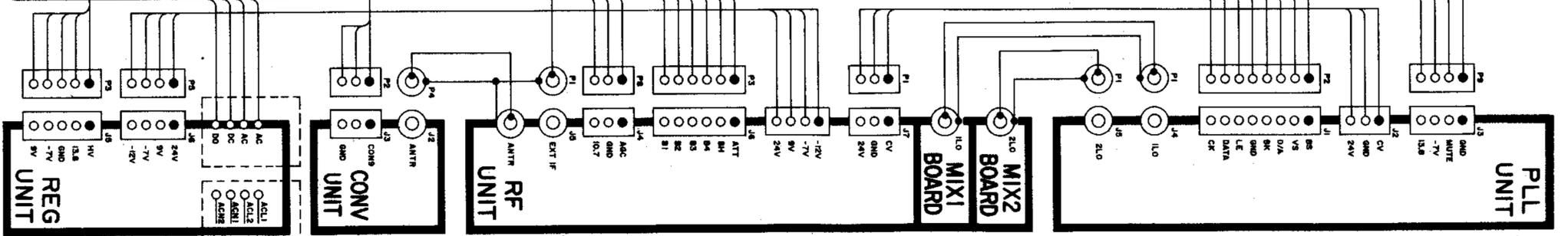
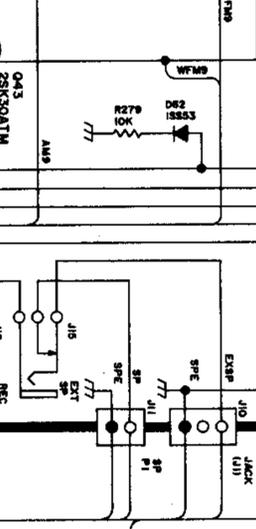
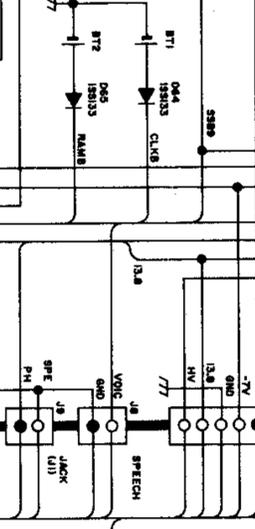
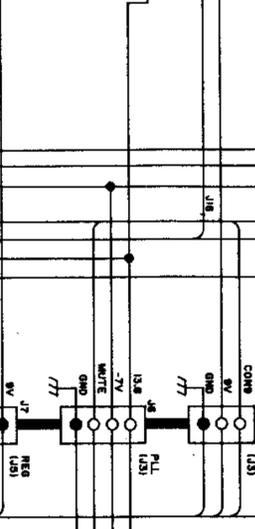
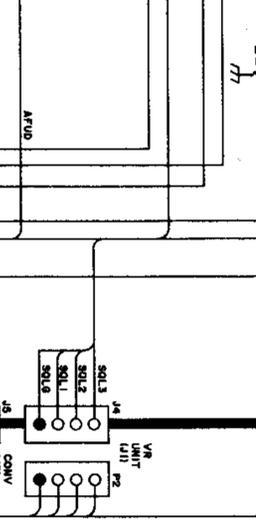
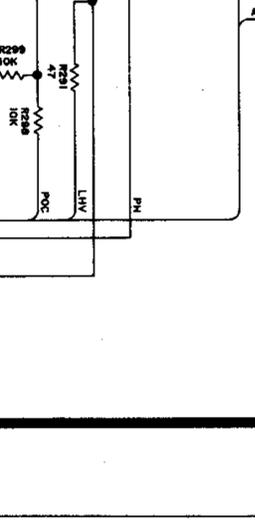
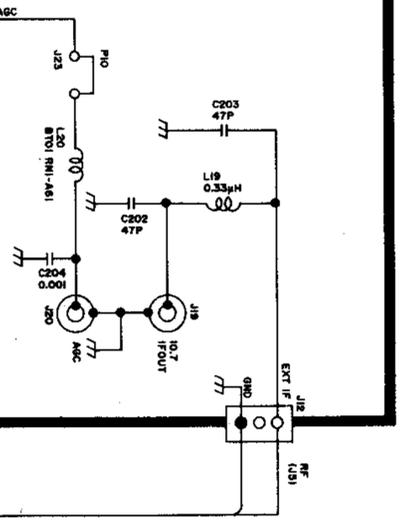
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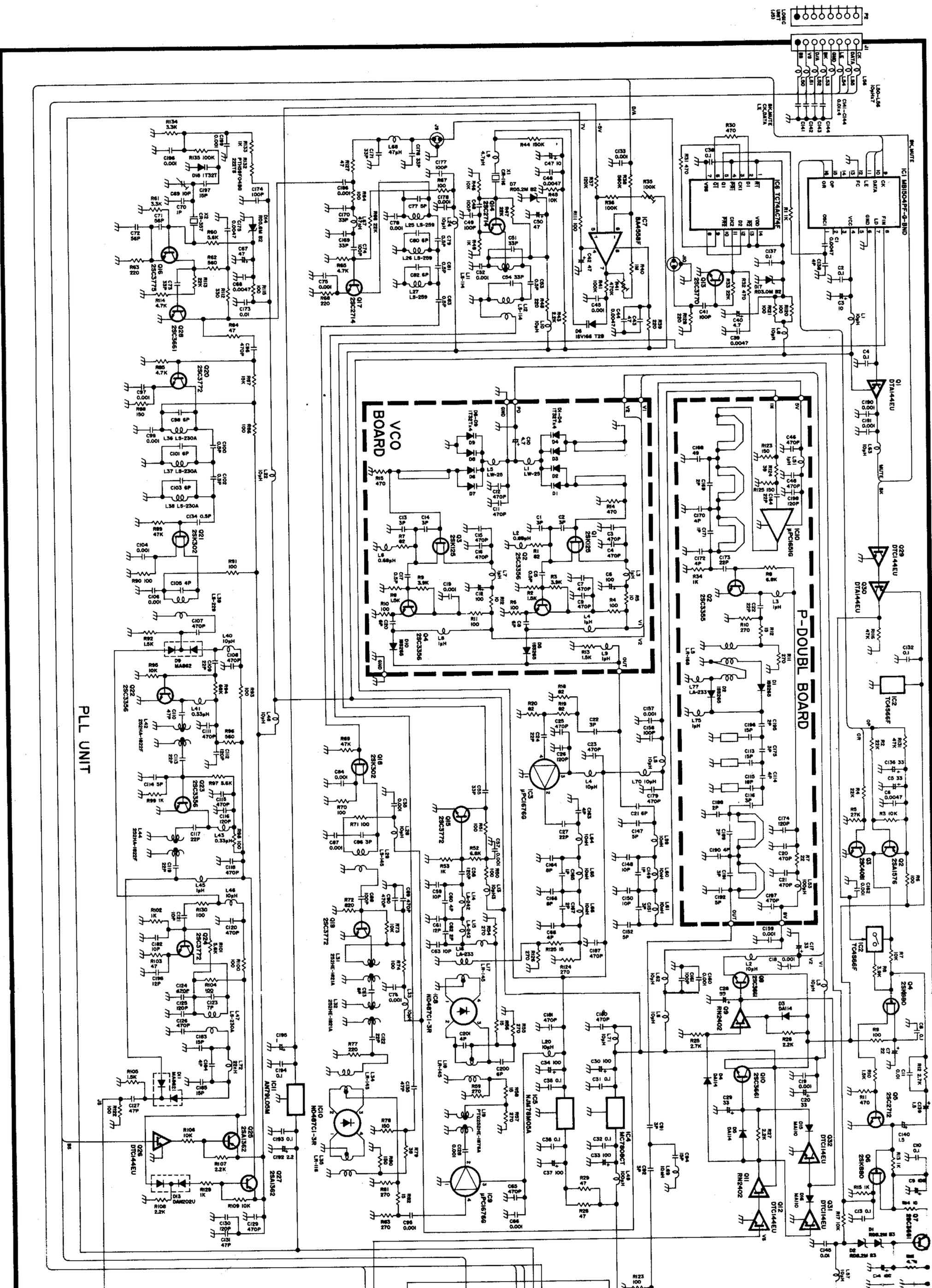
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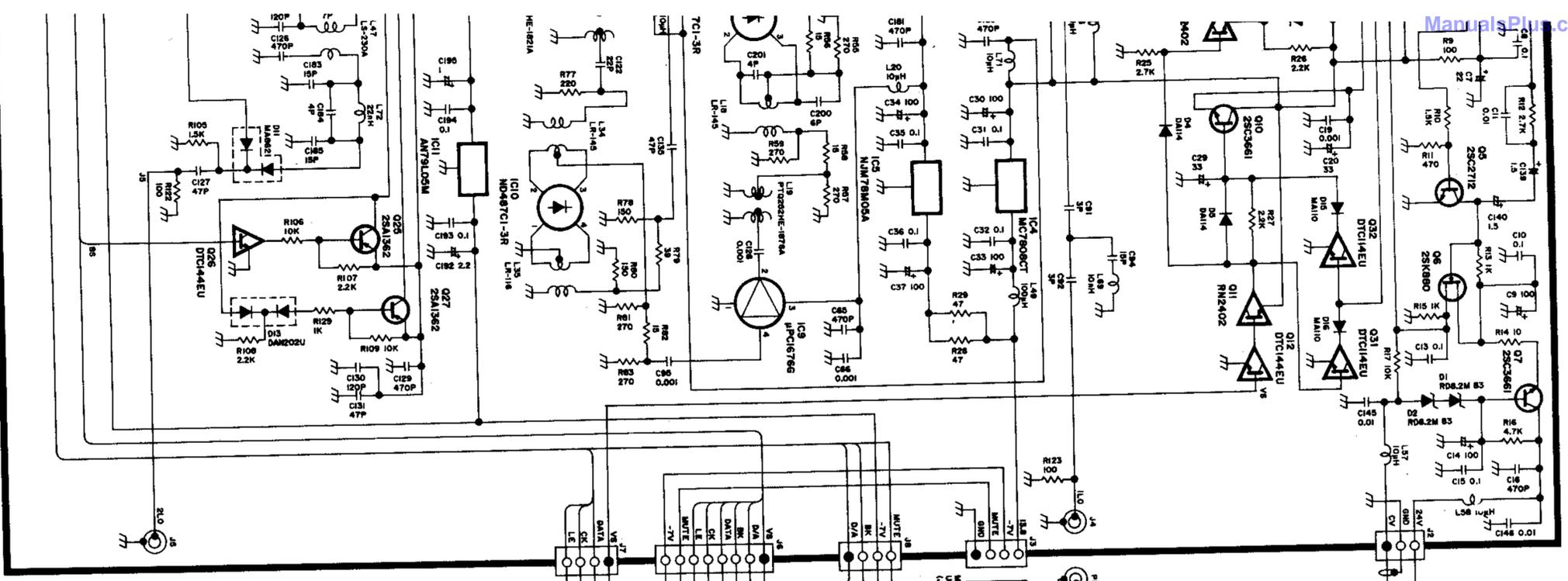
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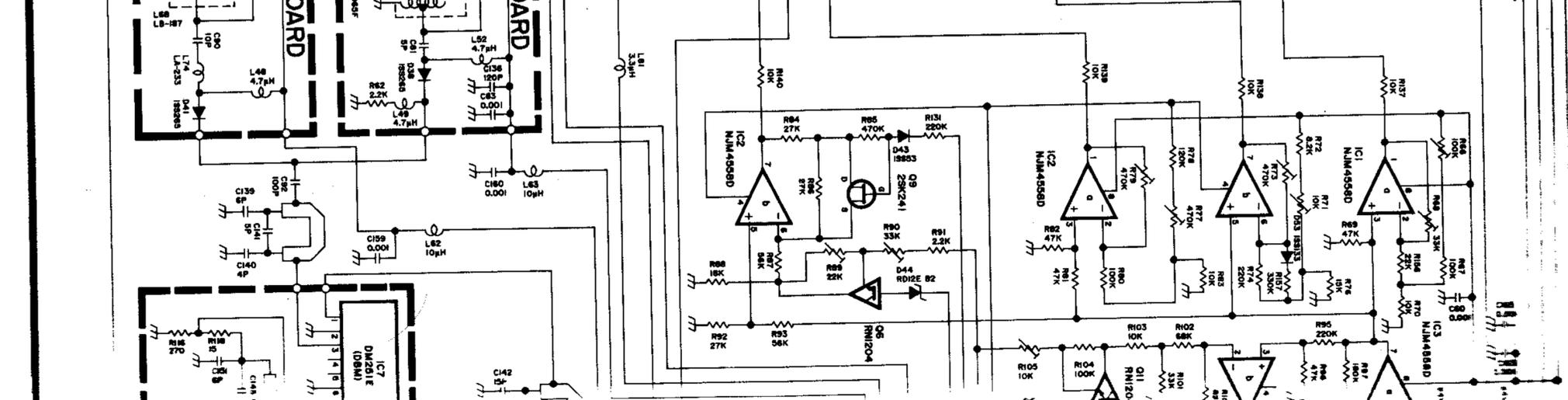
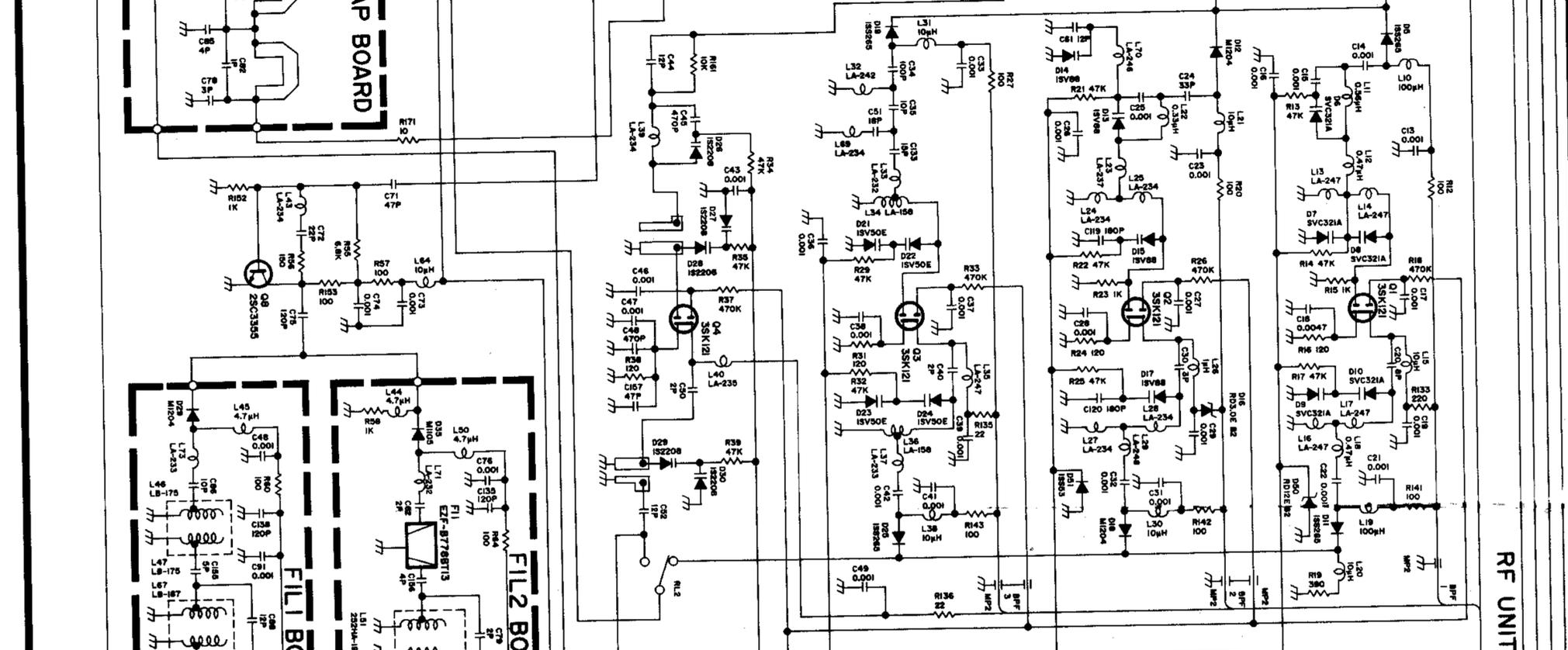
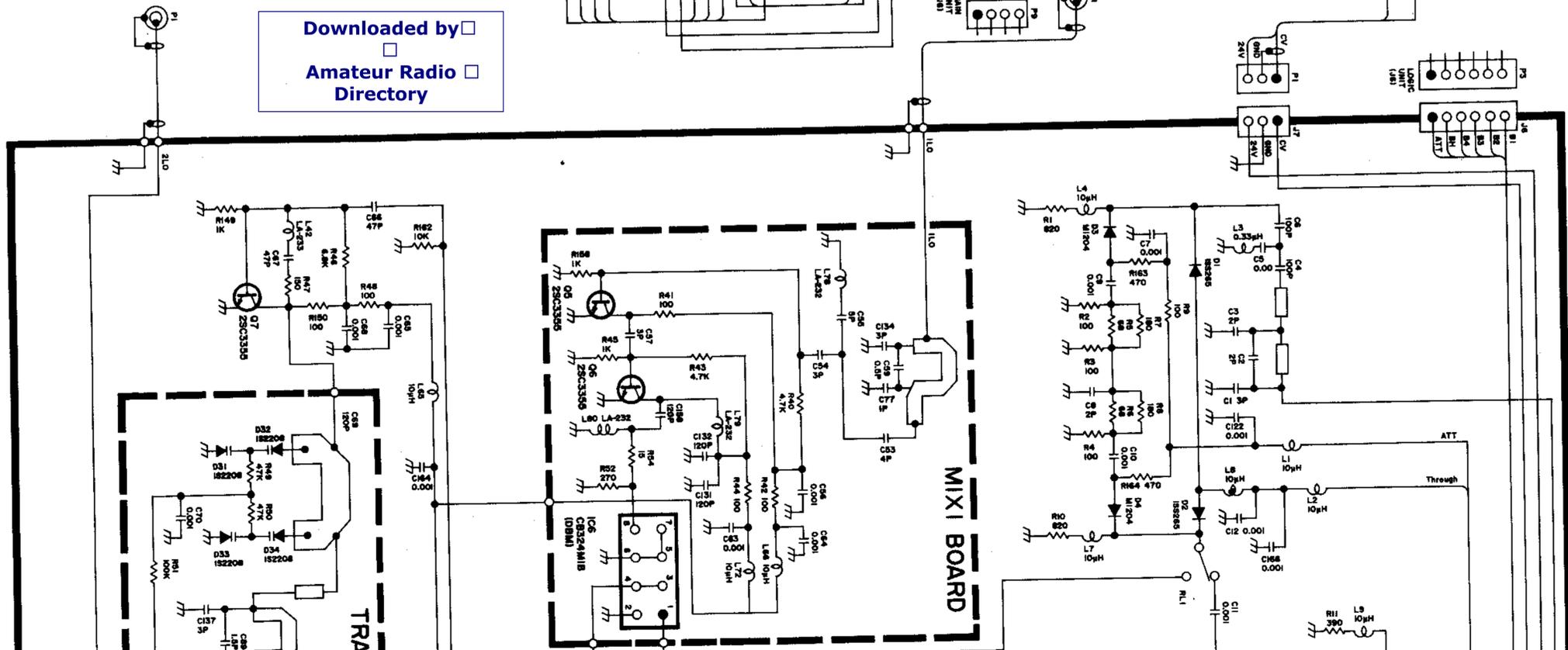
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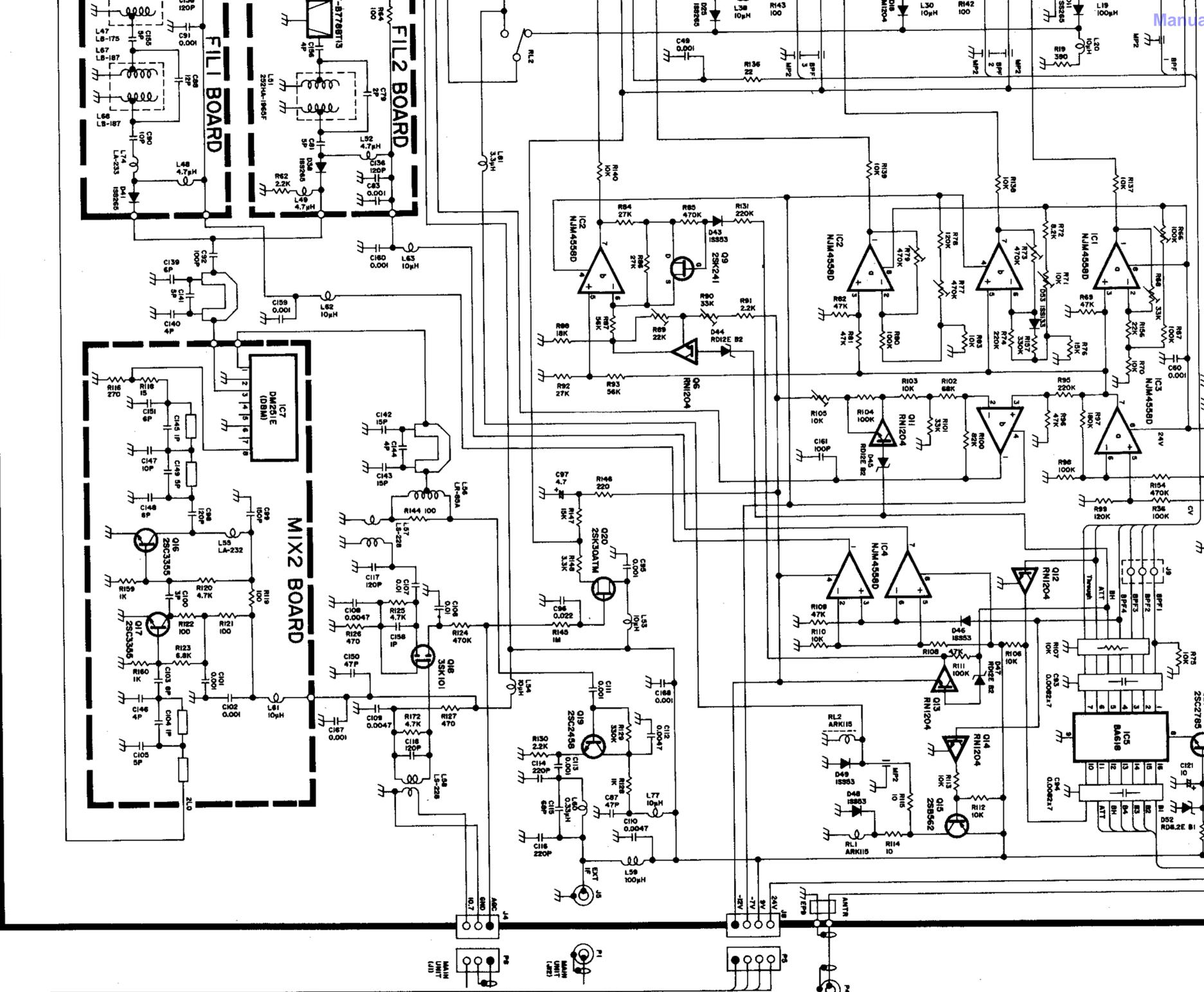




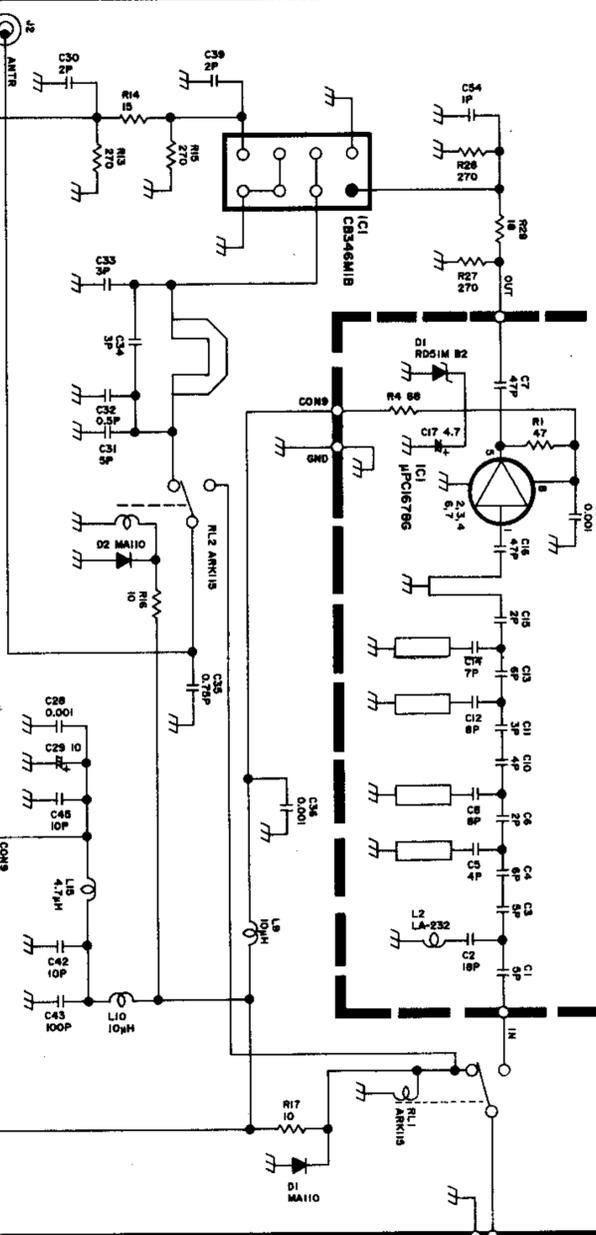
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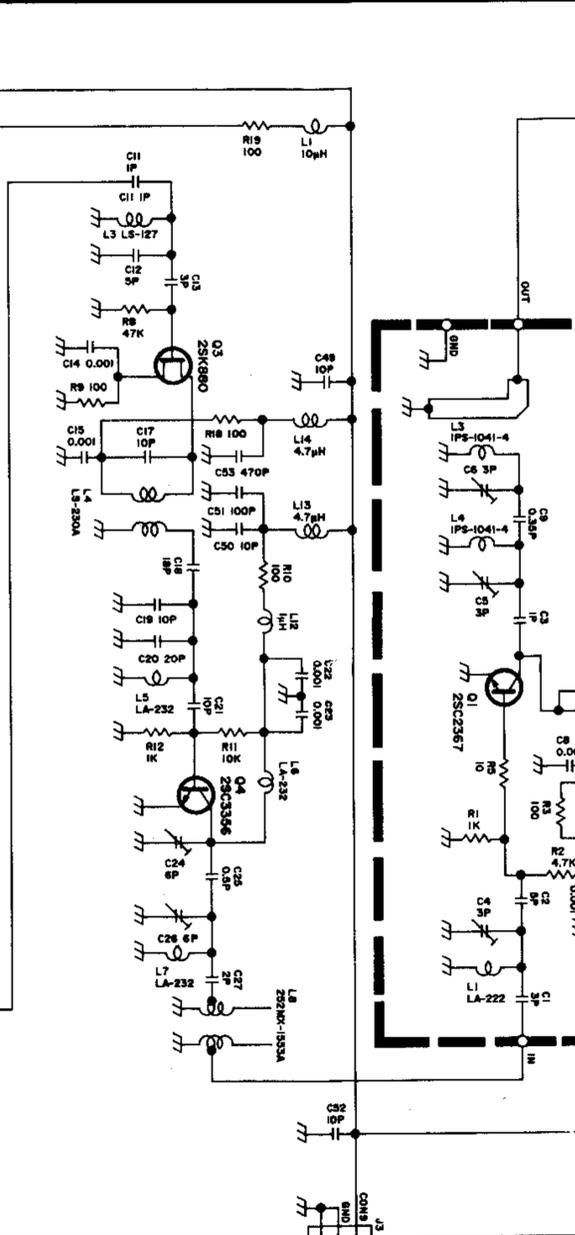
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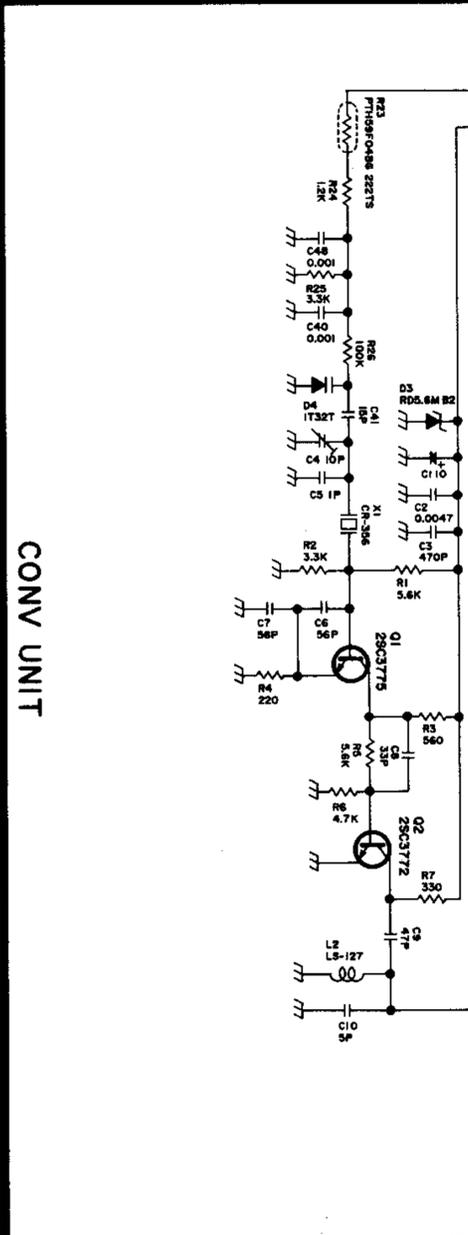
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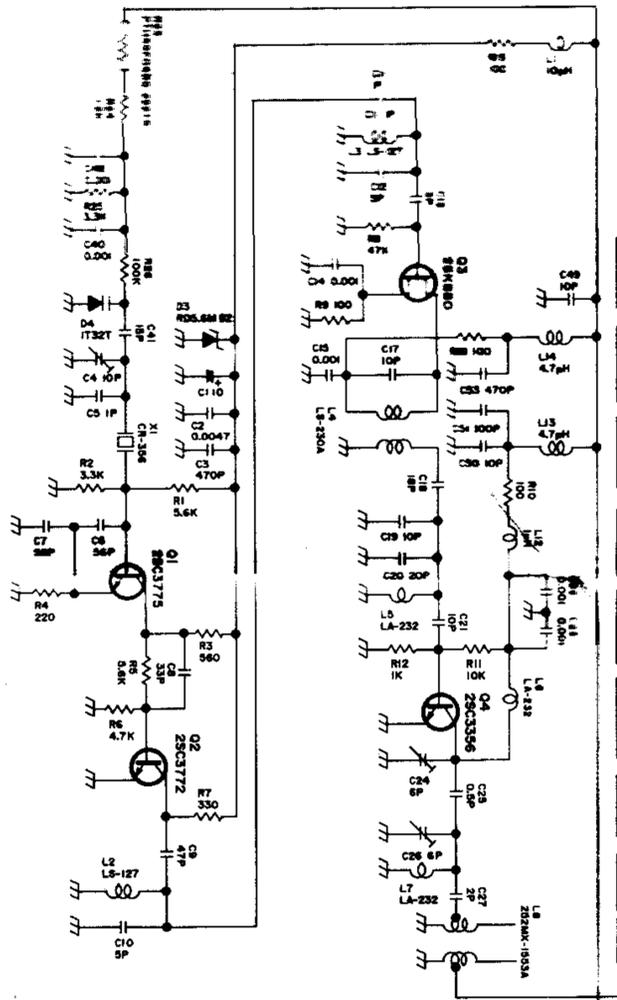
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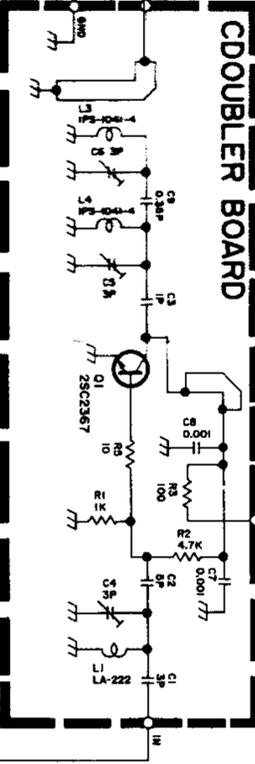
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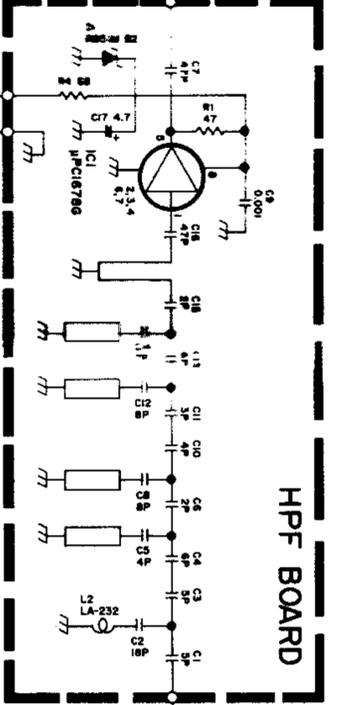
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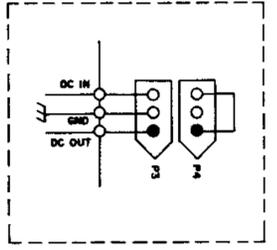
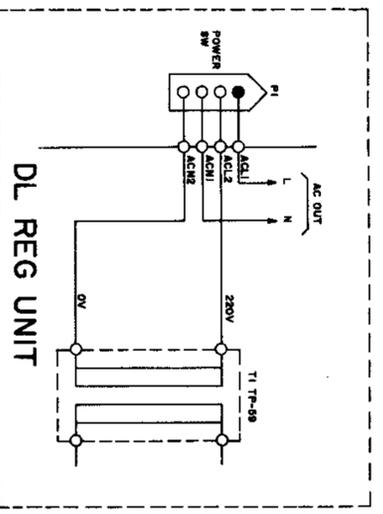
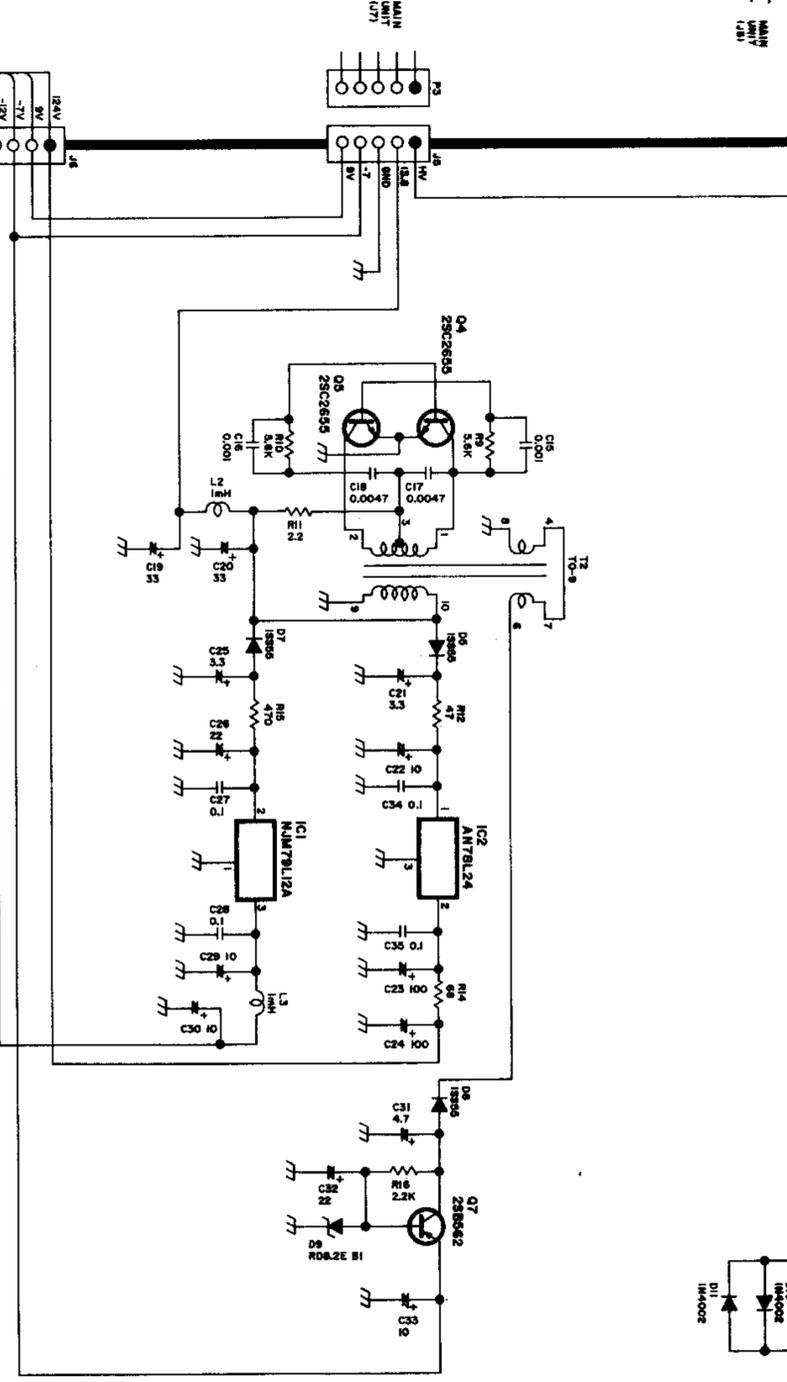
CDOUBLER BOARD



HPF BOARD



REG UNIT



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