

Revisions to SX-42 DWG 89D257

dwg zones 1-30 across top, A - X down side {1" spacing on 24x32 dwg }

Rev	Zone	Change	Date
		Released for Production	10/3/47
A	N-24	Add C133 .01uf bypass to AVC line near 2 nd IF grid circuit	10/13/47
B	A-28	Change C39 from 105 to 110 uuf in LO grid circuit	
	A-29	Change C49 from 105 to 110 uuf in LO plate decoupling	
	F-9	C8 (bypass cap below R1) deleted in AVC feed to 1 st RF amp grid	
	C-28	Add C120 7 uuf across band 6 local oscillator coil (T-18)	11/14/47
C	B-18	Delete R97, L7, between C25 and gnd in cathode ckt of 2 nd RF amp	11/24/47
	A-20	Change R96 from 680 to 330 ohms, 2 nd RF stage screen resistor	
	C-22	C123 changes from 22 uuf to 15 uuf across band 6 mixer input xfmr (T12) secondary	
D	O-22	Added C135 .01ufd, R107 2.2meg part of 455khz bandwidth switching for 3 rd IF coil at SW2-B	
	N-22	C80 2uuf changed to gimmic capacitor, near 3 rd IF coil, pin 6 this is bfo feed	
	L-28	Added C136 .005uf capacitor bypassing 6SK7 1 st IF heater to gnd)	
	O-3	Added C134 .01uf in parallel with C115 input filter capacitor	12/15/47
E	K-22	Deleted C76, from bottom of 10.7MHz 3 rd IF secondary to ground	
	L-28	Deleted C136, at 6SK7 1 st IF heater, (added under rev D)	
F		Switch 1-KK added contacts to switch cathode resistor of 7H7 -3 rd IF amp (function previously performed by SW-1K)	
		Switch 1-K now switches grid of 7H7 - 3 rd IF between 455khz and 10.7 Mhz secondaries of 3 rd IF coil (T26)	
		3 rd IF transformer and connections to it and SW-1K changed such that only one secondary at a time is connected to following grid	
		Pin 7 of 2 nd IF transformer (bottom of 455khz secondary) directly grounded (previously was allowed to float in bands 5 & 6, by SW-1K)	?1/10/48 ?
G	M-2	Add R108 6.8 ohm resistor in series with 6H6 noise limiter heater	2/14/48 ?

Notes: Revisions **E** and **F** apparently ran together. Prior to rev **E**, both the 10.7 Mhz and 455 kHz secondaries of 3rd IF coil (T-26) were connected, in series, to the grid of 7H7 - 3rd IF amplifier. Change to this configuration from the original design in which the 455kHz secondary fed a diode detector (half of 6H6 noise limiter) occurred with the initial release of 89D257. Prior configuration was drawing 89D210. Revisions to 89D210 summarized in Hallicrafters Service Hint #21, Oct '47.